


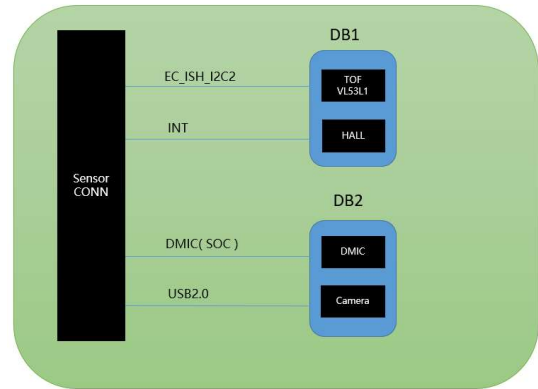
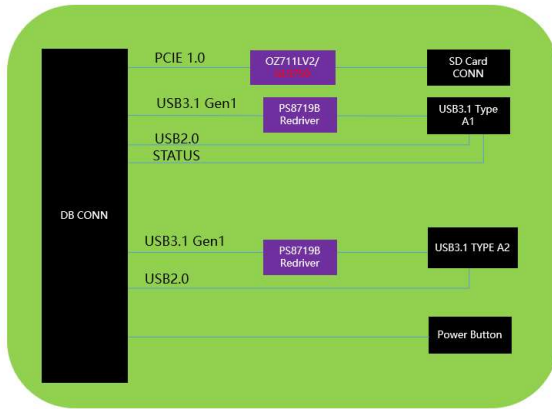
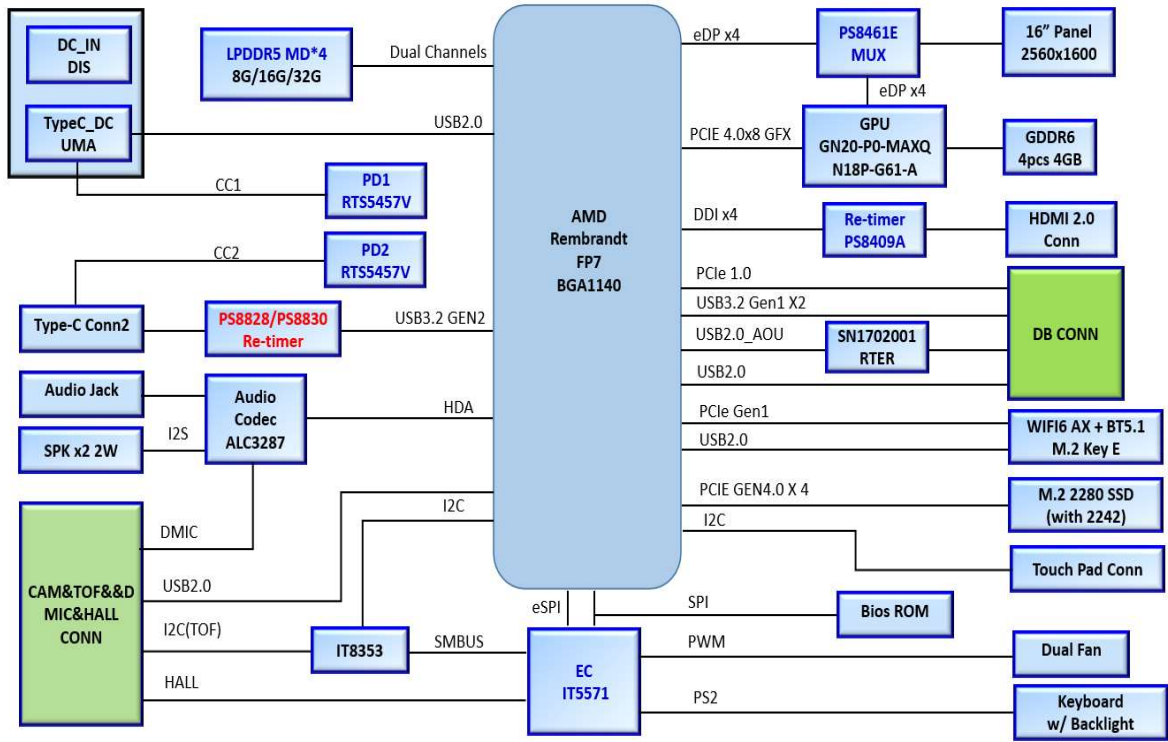
HuaQin Confidential

S570-16_NB6200_MB&DB Schematics Document

AMD FP7-DGPU N18P-G61/GN20-P0-MAXQ with LPDDR5 MD

REV5.0

 HUAQIN 华勤通信		Huaqin Telecom Technology Com.,Ltd.	
Page name: Cover page			
Size: A4	Project Name: NB6200	REV: V1.0	
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MEM DOWN ID

HW_ID3	HW_ID2	HW_ID1	HW_ID0	Description	Total
0	0	0	1	16Gb*32 LPDDR5 315ball 6400 Micron MT62F512M32D2DR-031 WT:B	8GB
0	0	1	0	16Gb*32 LPDDR5 315ball 6400 Hynix H9JCNBNNBK3MLYR-N6E	8GB
0	0	1	1	32Gb*32 LPDDR5 315ball 6400 Samsung K3LKBK08M-MGCP	16GB
0	1	0	0	32Gb*32 LPDDR5 315ball 6400 Micron MT62F1G32D4DR-031 WT:B	16GB
0	1	1	0	32Gb*32 LPDDR5 315ball 6400 Hynix H9JCNBNNCP3MLYR-N6E	16GB
0	1	1	1	64Gb*32 LPDDR5 315ball 6400 Samsung K3LKBK08M-MGCP	32GB
0	1	0	1	64Gb*32 LPDDR5 315ball 6400 Micron MT62F2G32D8DR-031 WT:B	32GB
1	0	0	0	64Gb*32 LPDDR5 315ball 6400 Hynix H9JCNBNNFA5MLYR-N6E	32GB
0	0	0	0	Reserve	Reserve
0	0	0	0	Reserve	Reserve

GPU ID

HW_ID5	HW_ID4	Description
0	0	UMA
0	1	GN20-P0-MAXQ
1	0	N18P-G61-A
1	1	GN20-P1 MAXQ

Phase ID

HW_ID7	HW_ID6	Description
0	0	EVT
0	1	FVT
1	0	SIT
1	1	SVT&SOVP

CPU ID

HW_ID8	Description
0	H CPU
1	HS CPU

Project ID


HW_ID11	HW_ID10	Description
0	0	S570 AMD
0	1	16PL AMD
1	0	S770 AMD

Retimer ID

HW_ID9	Description
0	PS8830
1	PS8828

SMBUS/I2C

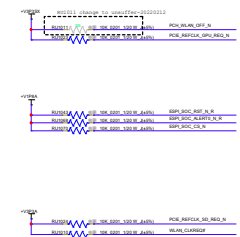
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SMB_EC_SCLK SMB_EC_SDATA	IT8987 +V3P3SX	X	X	X	X	X	V	X	X	X
SM_BAT_CLK SM_BAT_DATA	IT8987 +V3P3A_EC	X	X	V	V	X	X	X	X	X
SMB_PD_SCLK SMB_PD_SDATA	IT8987 +V3P3A_EC	X	X	X	X	V	X	X	X	X
I2C1_SCL_ALS I2C1_SDA_ALS	SOC +V3P3S	X	X	X	X	X	X	X	X	V
TOF_ISH1_SDA TOF_ISH1_SCL	IT81201 +V3P3A_TOF	X	X	X	X	X	X	V	X	X
TP_I2C_CLK TP_I2C_SDA	SOC +V3P3SX_TP	X	X	X	X	X	X	X	V	X
SMB_SEN_SCLK SMB_SEN_SDATA	SOC +V3P3SX	V	X	X	X	X	X	X	X	X

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Page name:		I2C Table	
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POWER RAIL WHICH IS RED ARE NOT USED BY APL
CONTINUED SEQUENCE CONTROL FOR THEM



		5		4		3		2		1	
UIUB											
						BANK OF 11					
						MEMORY CHD					
[23]	M_1_A_CA0		M_1_A_CA0	V32	MC1_D00	AA21	M_1_A_DQ_0<0>		M_1_A_DQ_0<0>	[23]	
[23]	M_1_A_CA1		M_1_A_CA1	V30	MC1_D01	W21	M_1_A_DQ_0<1>		M_1_A_DQ_0<1>	[23]	
[23]	M_1_A_CA2		M_1_A_CA2	T29	MC1_D02	Y21	M_1_A_DQ_0<2>		M_1_A_DQ_0<2>	[23]	
[23]	M_1_A_CA3		M_1_A_CA3	P31	MC1_D03	V21	M_1_A_DQ_0<3>		M_1_A_DQ_0<3>	[23]	
[23]	M_1_A_CA4		M_1_A_CA4	R30	MC1_D04	K20	M_1_A_DQ_0<4>		M_1_A_DQ_0<4>	[23]	
[23]	M_1_A_CA5		M_1_A_CA5	N31	MC1_D05	K21	M_1_A_DQ_0<5>		M_1_A_DQ_0<5>	[23]	
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					MC1_D07	J20	M_1_A_DQ_0<7>		M_1_A_DQ_0<7>	[23]	
					MC1_D08	K20	M_1_A_DQ_1<0>		M_1_A_DQ_1<0>	[23]	
					MC1_D09	M20	M_1_A_DQ_1<1>		M_1_A_DQ_1<1>	[23]	
					MC1_D010	L27	M_1_A_DQ_1<2>		M_1_A_DQ_1<2>	[23]	
					MC1_D011	M26	M_1_A_DQ_1<3>		M_1_A_DQ_1<3>	[23]	
					MC1_D012	R26	M_1_A_DQ_1<4>		M_1_A_DQ_1<4>	[23]	
					MC1_D013	P20	M_1_A_DQ_1<5>		M_1_A_DQ_1<5>	[23]	
					MC1_D014	R27	M_1_A_DQ_1<6>		M_1_A_DQ_1<6>	[23]	
					MC1_D015	T26	M_1_A_DQ_1<7>		M_1_A_DQ_1<7>	[23]	
[23]	M_1_A_CLK_DP		M_1_A_CLK_DP	T31	MC1_O0_7						
[23]	M_1_A_CLK_DN		M_1_A_CLK_DN	R32	MC1_O0_8						
[23]	M_1_B_CLK_DP		M_1_B_CLK_DP	V32	MC1_O0_7						
[23]	M_1_B_CLK_DN		M_1_B_CLK_DN	Y30	MC1_O0_8						
[23]	M_1_A_WCK0_DP		M_1_A_WCK0_DP	N21	MC1_WCK_0						
[23]	M_1_A_WCK0_DN		M_1_A_WCK0_DN	N22	MC1_WCK_0						
[23]	M_1_A_WCK1_DP		M_1_A_WCK1_DP	P27	MC1_WCK_1						
[23]	M_1_A_WCK1_DN		M_1_A_WCK1_DN	P26	MC1_WCK_1						
[23]	M_1_B_WCK0_DP		M_1_B_WCK0_DP	V23	MC1_WCK_0						
[23]	M_1_B_WCK0_DN		M_1_B_WCK0_DN	W24	MC1_WCK_0						
[23]	M_1_B_WCK1_DP		M_1_B_WCK1_DP	W27	MC1_WCK_1						
[23]	M_1_B_WCK1_DN		M_1_B_WCK1_DN	W26	MC1_WCK_1						
[23]	M_1_A_CS0		M_1_A_CS1	T30	MC1_CS0						
[23]	M_1_A_CS1		M_1_A_CS1	U31	MC1_CS1						
[23]	M_1_B_CS0		M_1_B_CS1	W31	MC1_CS0						
[23]	M_1_B_CS1		M_1_B_CS1	W30	MC1_CS1						
[23]	M_1_A_DM0		M_1_A_DM1	T21	MC1_DM0&MC1_DM0						
[23]	M_1_A_DM1		M_1_A_DM1	M27	MC1_DM0&MC1_DM1						
[23]	M_1_B_DM0		M_1_B_DM1	W23	MC1_DM0&MC1_DM0						
[23]	M_1_B_DM1		M_1_B_DM1	W22	MC1_DM0&MC1_DM1						
[23]	M_1_AB_RESET_N		M_1_AB_RESET_N	N29	MC1_RESET_L						
18MIL	TPU701	qds_1	M_1_AB_TP	C32	MC1_TEST						
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[24]	M_1_C_CA1		M_1_C_CA1	AG29	MC1_C01	AC29	M_1_C_DQ_1<1>		M_1_C_DQ_1<1>	[24]	
[24]	M_1_C_CA2		M_1_C_CA2	AF31	MC1_C02	AD27	M_1_C_DQ_1<2>		M_1_C_DQ_1<2>	[24]	
[24]	M_1_C_CA3		M_1_C_CA3	AG30	MC1_C03	AC27	M_1_C_DQ_1<3>		M_1_C_DQ_1<3>	[24]	
[24]	M_1_C_CA4		M_1_C_CA4	AD31	MC1_C04	AC27	M_1_C_DQ_1<4>		M_1_C_DQ_1<4>	[24]	
[24]	M_1_C_CA5		M_1_C_CA5	AC31	MC1_C05	AC26	M_1_C_DQ_1<5>		M_1_C_DQ_1<5>	[24]	
[24]	M_1_C_CA6		M_1_C_CA6	AD30	MC1_C06	AC27	M_1_C_DQ_1<6>		M_1_C_DQ_1<6>	[24]	
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					MC1_C08	AD29	M_1_C_DQ_2<0>		M_1_C_DQ_2<0>	[24]	
					MC1_C09	AD24	M_1_C_DQ_2<1>		M_1_C_DQ_2<1>	[24]	
					MC1_C10	AD23	M_1_C_DQ_2<2>		M_1_C_DQ_2<2>	[24]	
					MC1_C11	AF23	M_1_C_DQ_2<3>		M_1_C_DQ_2<3>	[24]	
					MC1_C12	AC22	M_1_C_DQ_2<4>		M_1_C_DQ_2<4>	[24]	
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					MC1_C14	AD23	M_1_C_DQ_2<6>		M_1_C_DQ_2<6>	[24]	
					MC1_C15	AC24	M_1_C_DQ_2<7>		M_1_C_DQ_2<7>	[24]	
[24]	M_1_D_CA0		M_1_D_CA0	AG30	MC1_D00						
[24]	M_1_D_CA1		M_1_D_CA1	AH31	MC1_D01						
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[24]	M_1_D_CA3		M_1_D_CA3	AK30	MC1_D03						
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[24]	M_1_C_CLK_DN		M_1_C_CLK_DN	AE32	MC1_O0_8						
[24]	M_1_D_CLK_DP		M_1_D_CLK_DP	AK32	MC1_O0_7						
[24]	M_1_D_CLK_DN		M_1_D_CLK_DN	AJ31	MC1_O0_8						
[24]	M_1_C_WCK1_DP		M_1_C_WCK1_DP	AF27	MC1_WCK_0						
[24]	M_1_C_WCK1_DN		M_1_C_WCK1_DN	AF26	MC1_WCK_0						
[24]	M_1_C_WCK0_DP		M_1_C_WCK0_DP	AE24	MC1_WCK_1						
[24]	M_1_C_WCK0_DN		M_1_C_WCK0_DN	AE23	MC1_WCK_1						
[24]	M_1_D_WCK1_DP		M_1_D_WCK1_DP	AL26	MC1_WCK_0						
[24]	M_1_D_WCK1_DN		M_1_D_WCK1_DN	AL27	MC1_WCK_0						
[24]	M_1_D_WCK0_DP		M_1_D_WCK0_DP	AM21	MC1_WCK_1						
[24]	M_1_D_WCK0_DN		M_1_D_WCK0_DN	AM22	MC1_WCK_1						
[24]	M_1_C_CS0		M_1_C_CS1	AF30	MC1_CS0						
[24]	M_1_C_CS1		M_1_C_CS1	AG31	MC1_CS1						
[24]	M_1_D_CS0		M_1_D_CS1	AJ29	MC1_CS0						
[24]	M_1_D_CS1		M_1_D_CS1	AJ30	MC1_CS1						
[24]	M_1_C_DM1		M_1_C_DM0	AF29	MC1_DM0&MC1_DM0						
[24]	M_1_C_DM0		M_1_C_DM0	AG24	MC1_DM0&MC1_DM1						
[24]	M_1_D_DM1		M_1_D_DM1	AN27	MC1_DM0&MC1_DM0						
[24]	M_1_D_DM0		M_1_D_DM0	AJ21	MC1_DM0&MC1_DM1						
[24]	M_1_CD_RESET_N		M_1_CD_RESET_N	AC30	MC1_RESET_L						
18MIL	TPU702	qds_1	M_1_CD_TP	B832	MC1_TEST						
FP7											
FP7 REV05.01											
HUAQIN											
Huaqin Telecom Technology Co., Ltd.											
Page name: FP7 Memory CHB											
Size: A4 Project Name: NB6200 REV: V1.0											
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For AMD suggestion, pull 10K to S5

USB2.0 to Type-C1

[61] USB2_TYPEC1_DP USB2_TYPEC1_DP D13
[61] USB2_TYPEC1_DN USB2_TYPEC1_DN B13 3.3V_85

USB2.0 to Type-C2

[66] USB2_TYPEC2_DP USB2_TYPEC2_DP D11
[66] USB2_TYPEC2_DN USB2_TYPEC2_DN B11 3.3V_85

Finger Print

[68] USB2_FP_DP USB2_FP_DP C15
[68] USB2_FP_DN USB2_FP_DN A15 3.3V_85

TYPEA1-AOU DB

[55] USB2_TYPEA1_DP USB2_TYPEA1_DP D9
[55] USB2_TYPEA1_DN USB2_TYPEA1_DN B9 3.3V_85

TYPEA2 DB

[87] USB2_TYPEA2_DP USB2_TYPEA2_DP D5
[87] USB2_TYPEA2_DN USB2_TYPEA2_DN E5 3.3V_85

USB to BT

[50] USB2_BT_DP USB2_BT_DP L10
[50] USB2_BT_DN USB2_BT_DN L9 3.3V_85

USB to CAM

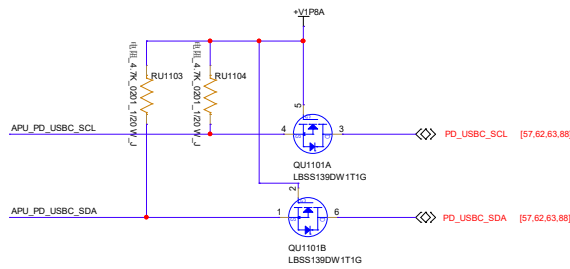
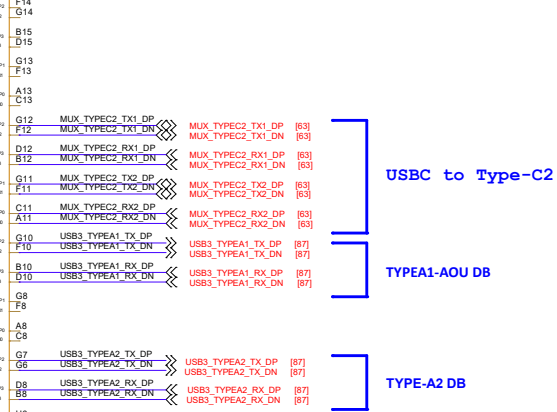
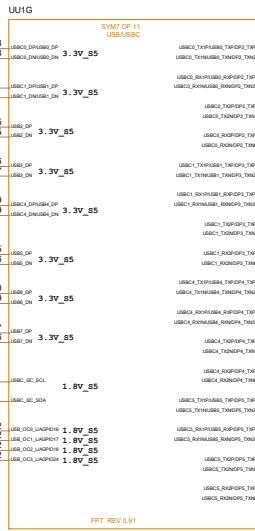
[76] USB2_CAM_DP USB2_CAM_DP K7
[76] USB2_CAM_DN USB2_CAM_DN K6 3.3V_85

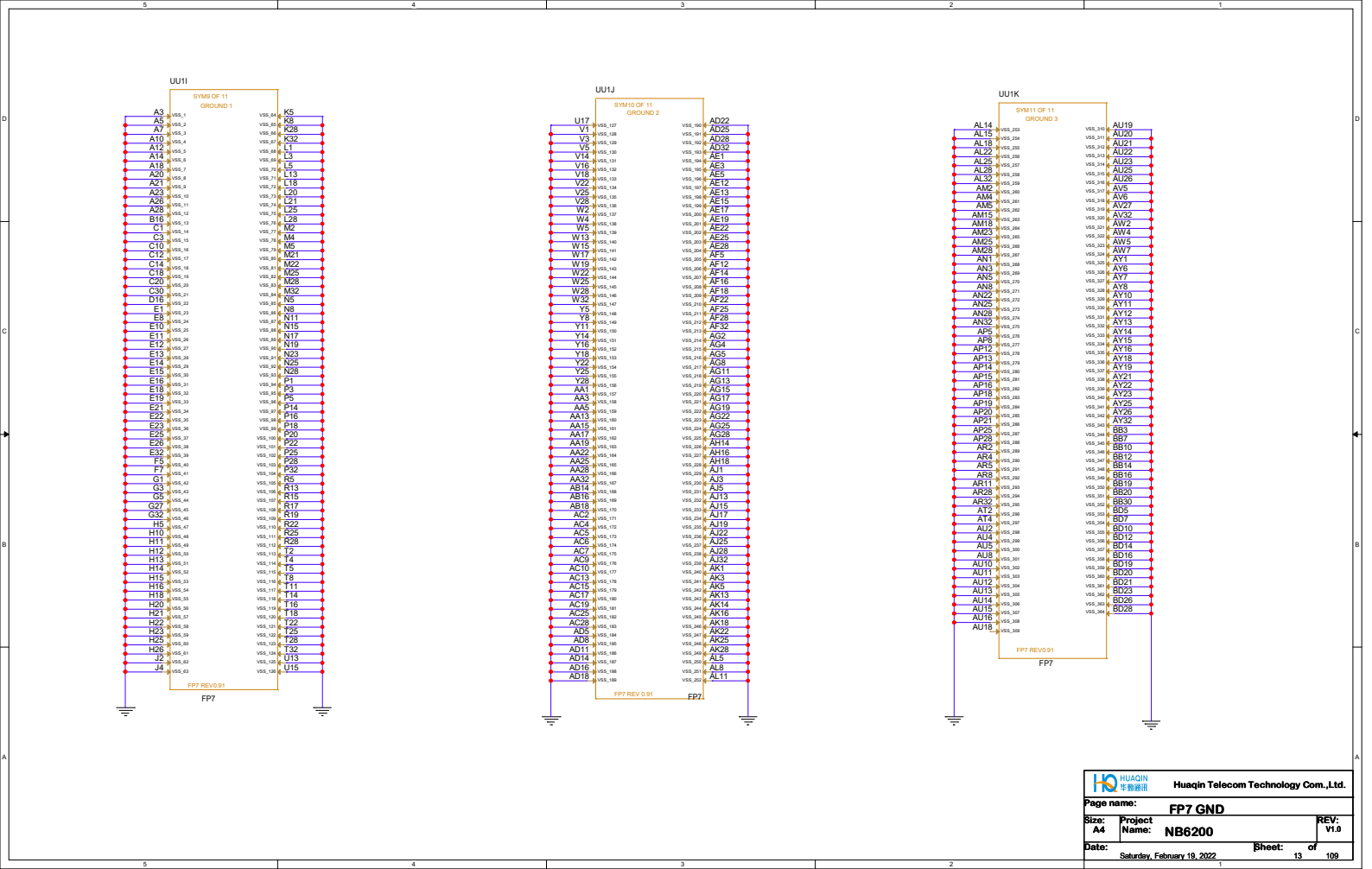
I2C to PD

APU_PD_USBC_SCL K11 1.8V_85
APU_PD_USBC_SDA J11 1.8V_85

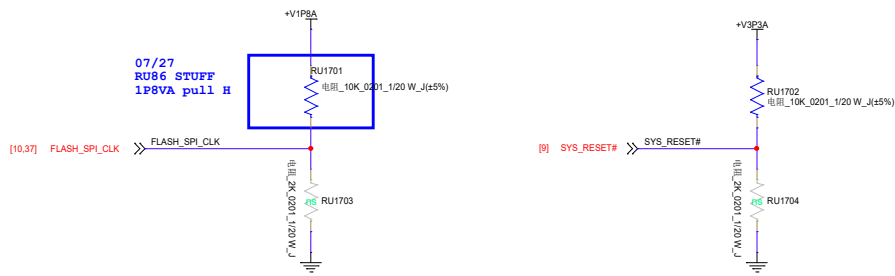
[50] PCIE_WLAN_WAKE_N
For Modern Standby

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[55] USB_A2_OC2 USB_A2_OC2 V12 1.8V_85
[55] WLAN_WAKE_N_SDC AA15 1.8V_85
[55] USB_OC3_L AC12 1.8V_85






STRAP PINS

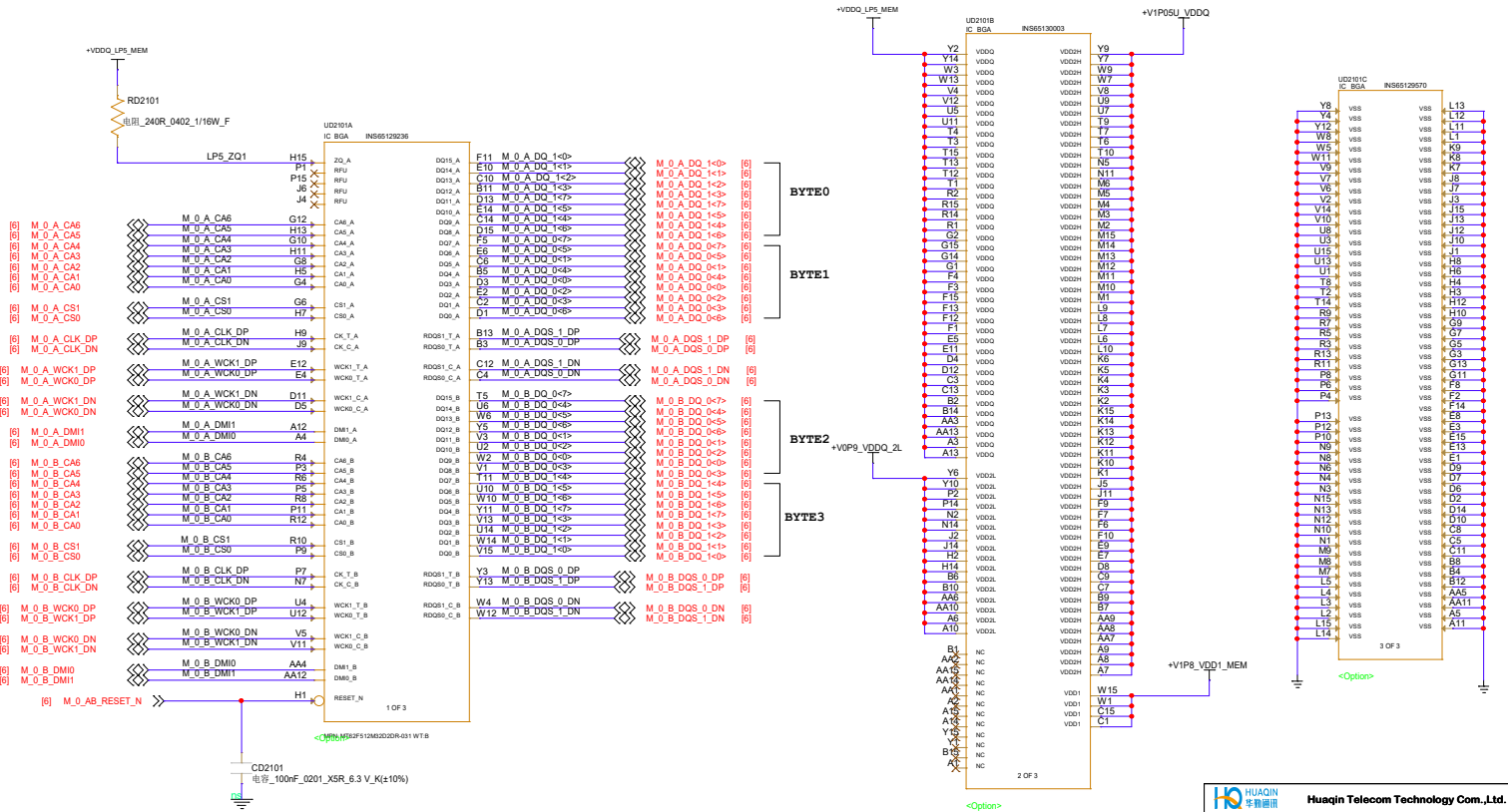


STRAP	FUNCTION	DEFINITION
SPI_CLK		1:USE 48MHZ CRYSTAL CLOCK AND GENERATE BOTH INTERNAL AND EXTERNAL CLOCKS (DEFAULT) 0:USE 100MHZ PCIE CLOCK AS REFERENCE CLOCK AND GENERATE INTERNAL CLOCKS ONLY
SYS_RST#		1:NORMAL RESET MODE (DEFAULT) 0:SHORT RESET MODE

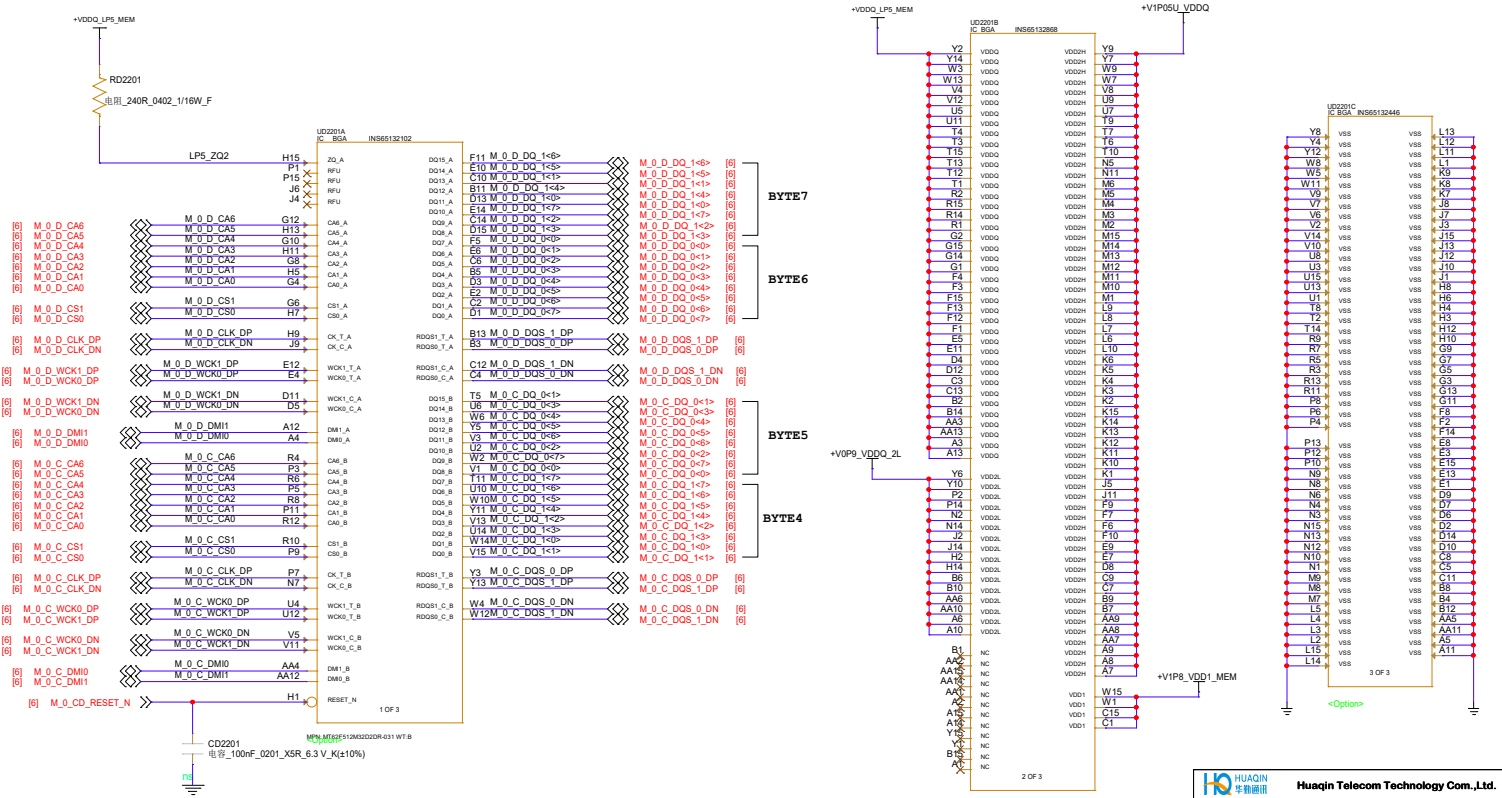
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D				
C				
B				
A				
5	4	3	2	1

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Page name:			
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Size: A4	Project Name:	REV: V1.0	
	NB6200		
Date:	Sheet:		of
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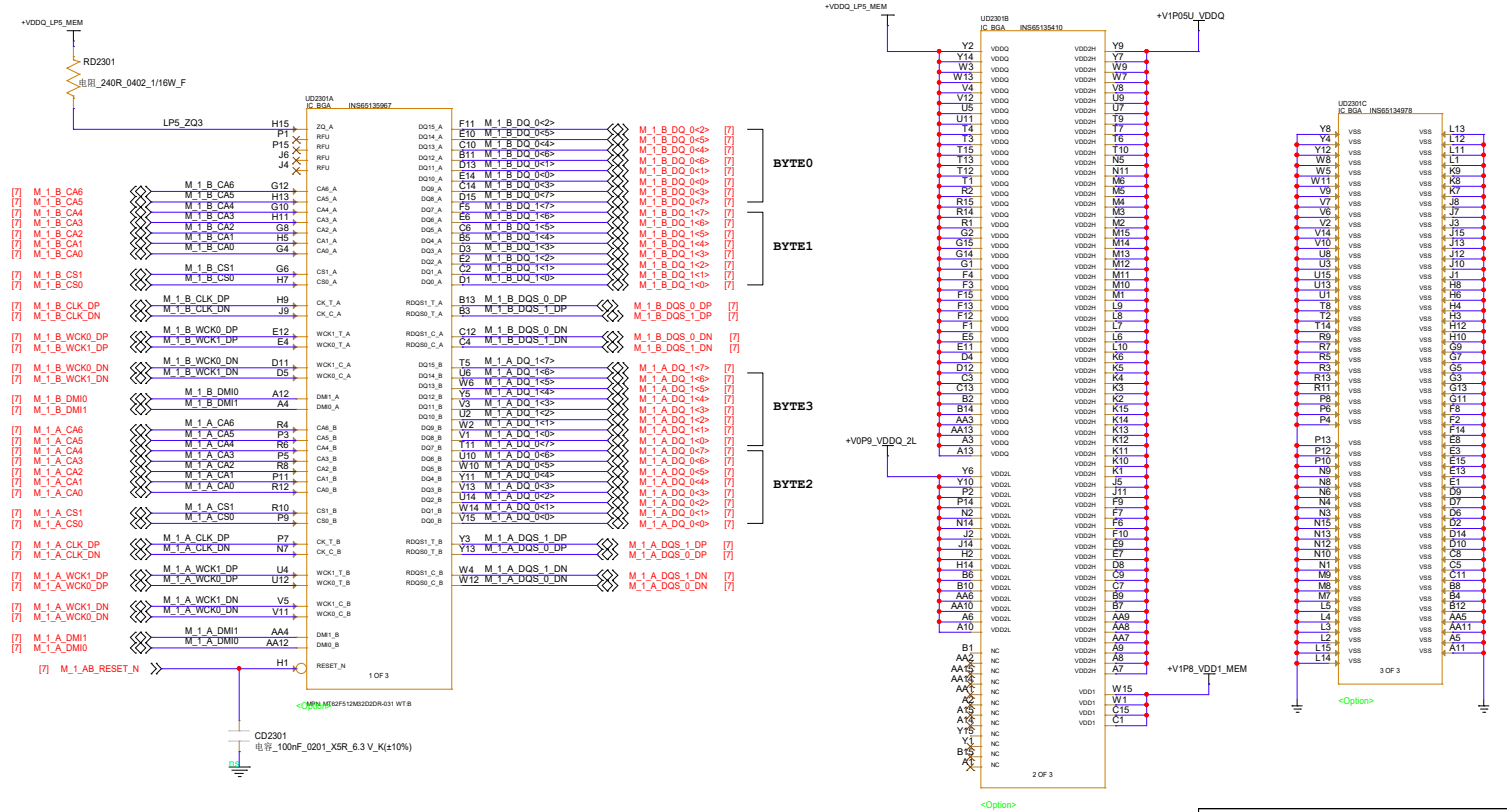
CHA-1



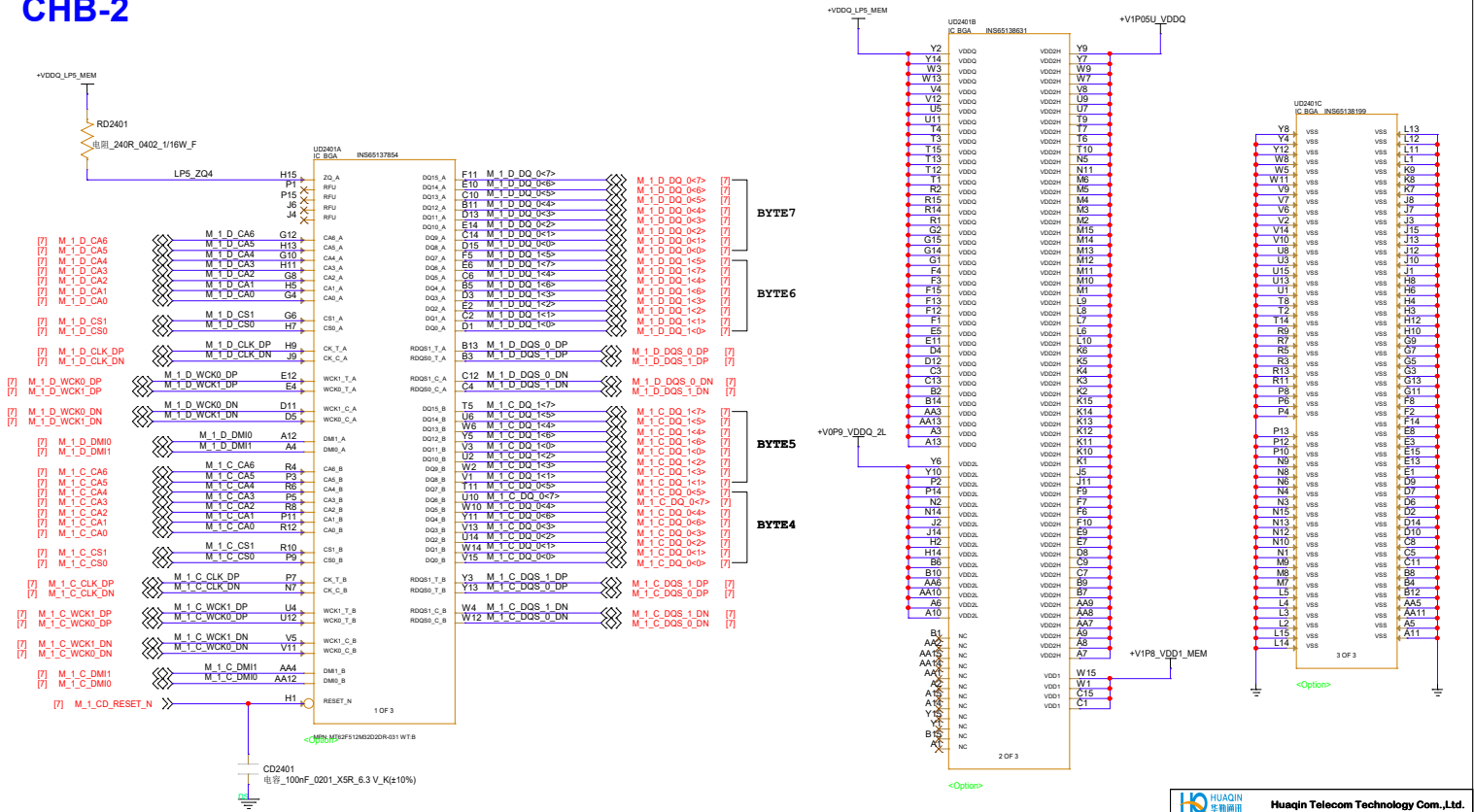
CHA-2



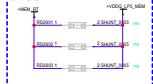
CHB-1



CHB-2



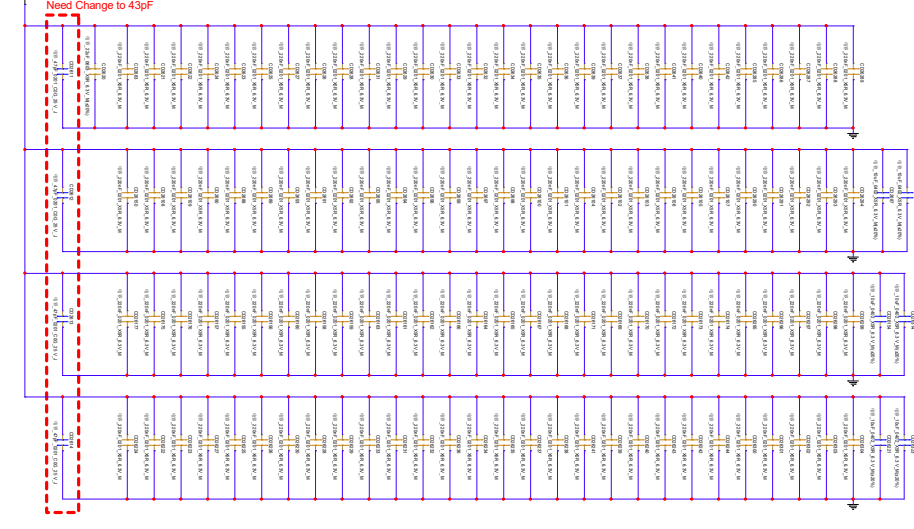
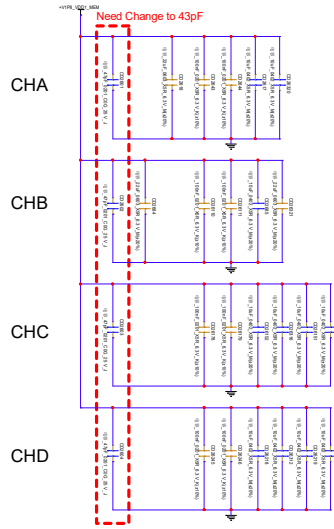
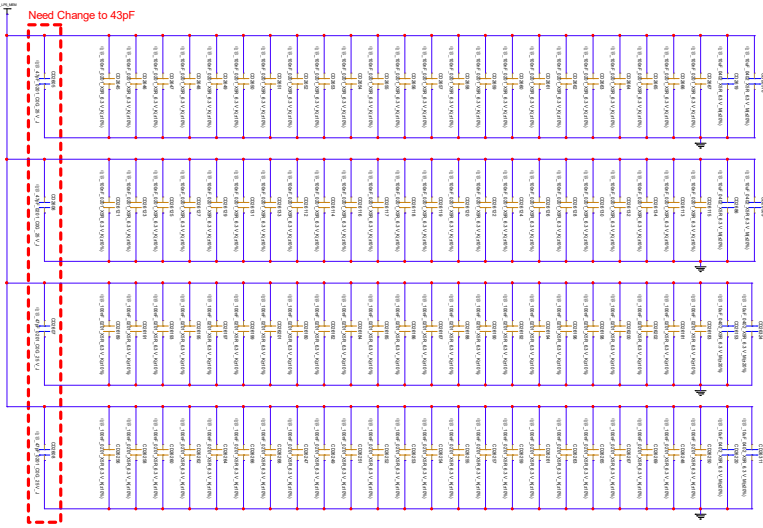
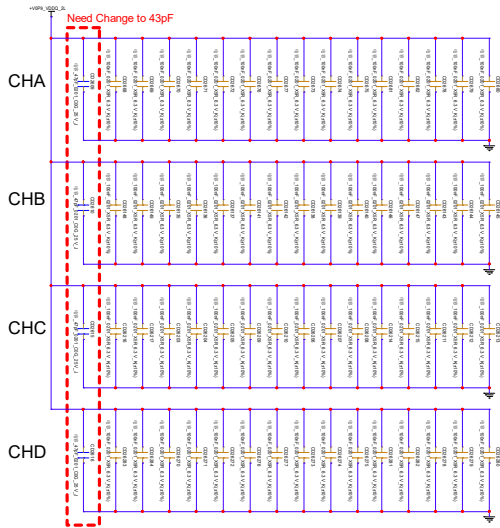
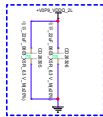
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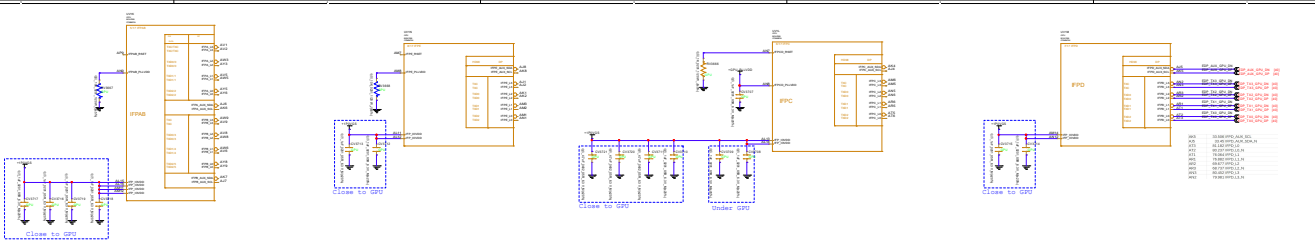


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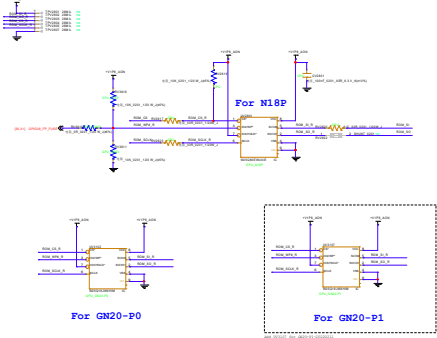
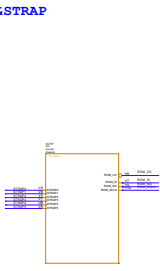


1P8SX

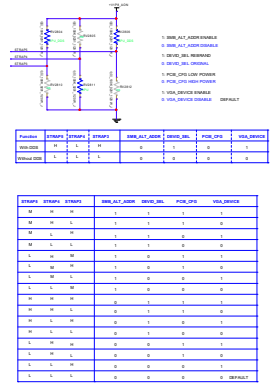
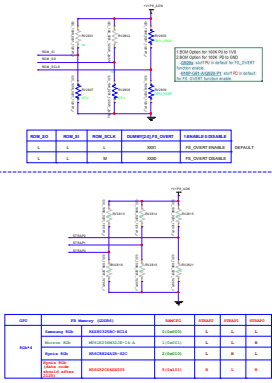




SPI&STRAP



UV2801/UV3102/UV3107 Co-layer design

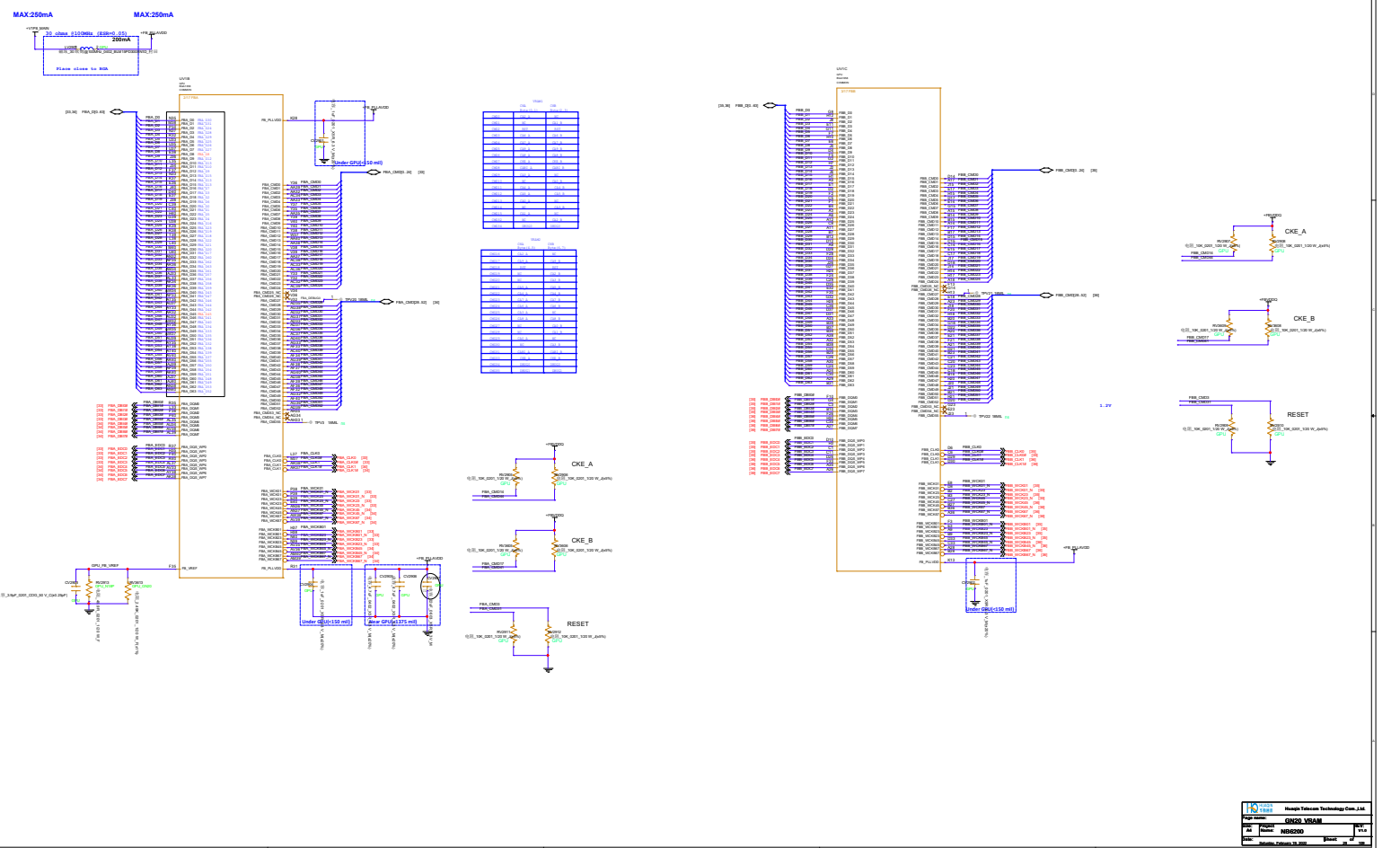


N18P-G5 GPD (Update later)

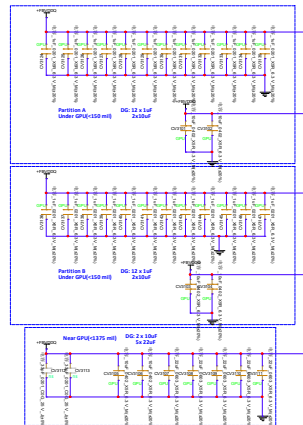
SPINUM	IO#	ACTIVE	Function Description	IO# Notation
SPIN01	OUT1	-	PMW Output to Control WLED	
SPIN02	OUT1	-	PMW Output to SDC 2.1	
SPIN03	IN	-	SPW Input to SDC 2.1	
SPIN04	OUT1	-	PMW Output to Control the WLED power supply	
SPIN05	OUT1	-	SPW Input to SDC 2.1 - PMW, WLED, SW	
SPIN06	IN	-	Power Switching, WLED, PM	
SPIN07	OUT1	NA	Power Switching enable	
SPIN08	OUT1	-	Power Switching Control	
SPIN09	OUT1	-	Power Switching Control (200K pull down)	
SPIN10	OUT1	-	Power Switching Control (200K pull down)	
SPIN11	IN	-	Power Switching Control (200K pull down)	
SPIN12	IN	-	Power Switching Control (200K pull down)	
SPIN13	IN	-	Power Switching Control (200K pull down)	
SPIN14	IN	-	Power Switching Control (200K pull down)	
SPIN15	IN	-	Power Switching Control (200K pull down)	
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SPIN18	IN	-	Power Switching Control (200K pull down)	
SPIN19	IN	-	Power Switching Control (200K pull down)	
SPIN20	IN	-	Power Switching Control (200K pull down)	
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SPIN98	IN	-	Power Switching Control (200K pull down)	
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SPIN100	IN	-	Power Switching Control (200K pull down)	

N18P-G5 Max-Q Power Sequence (Update later)

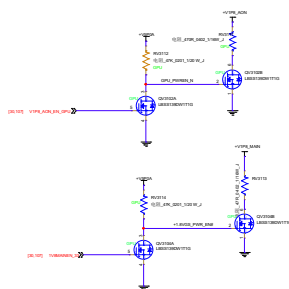
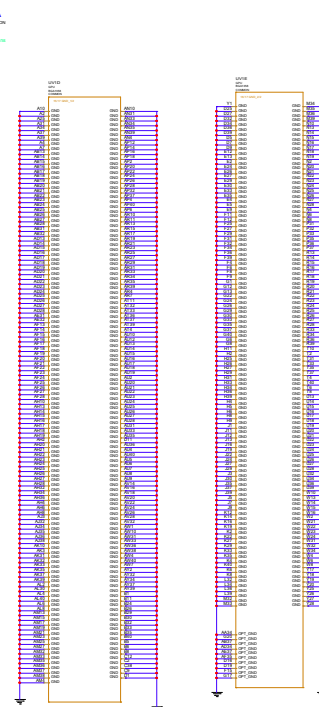
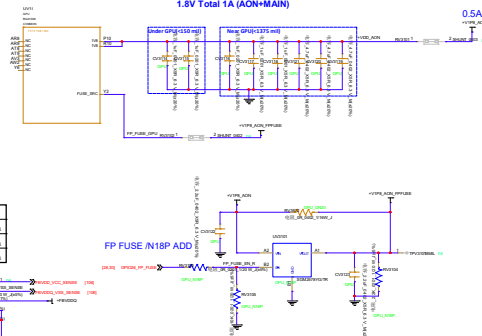
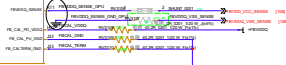


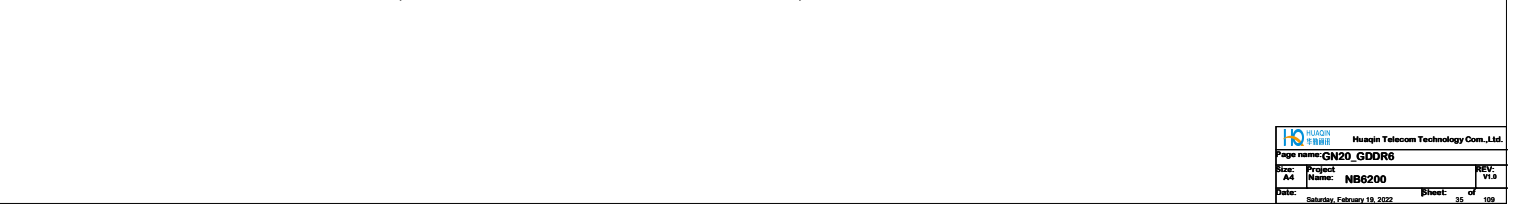
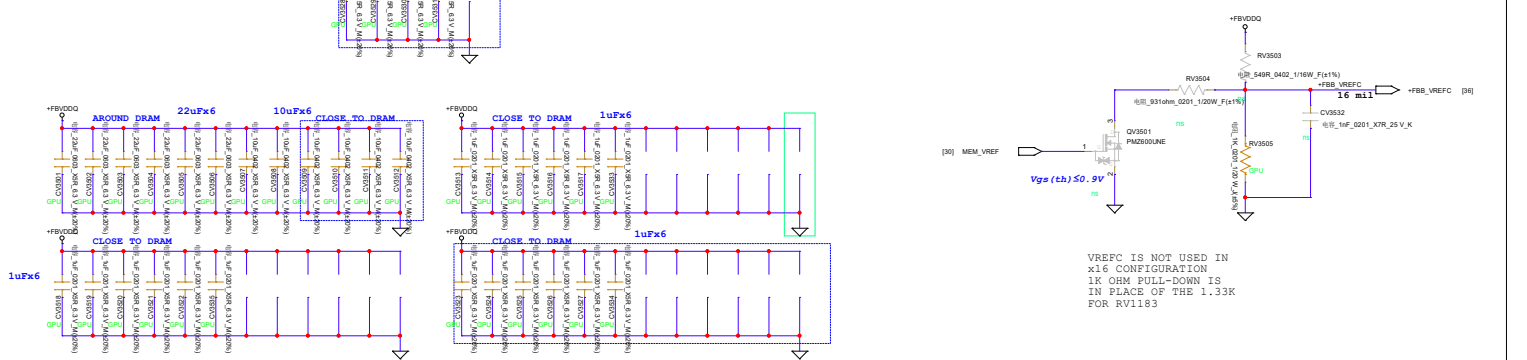
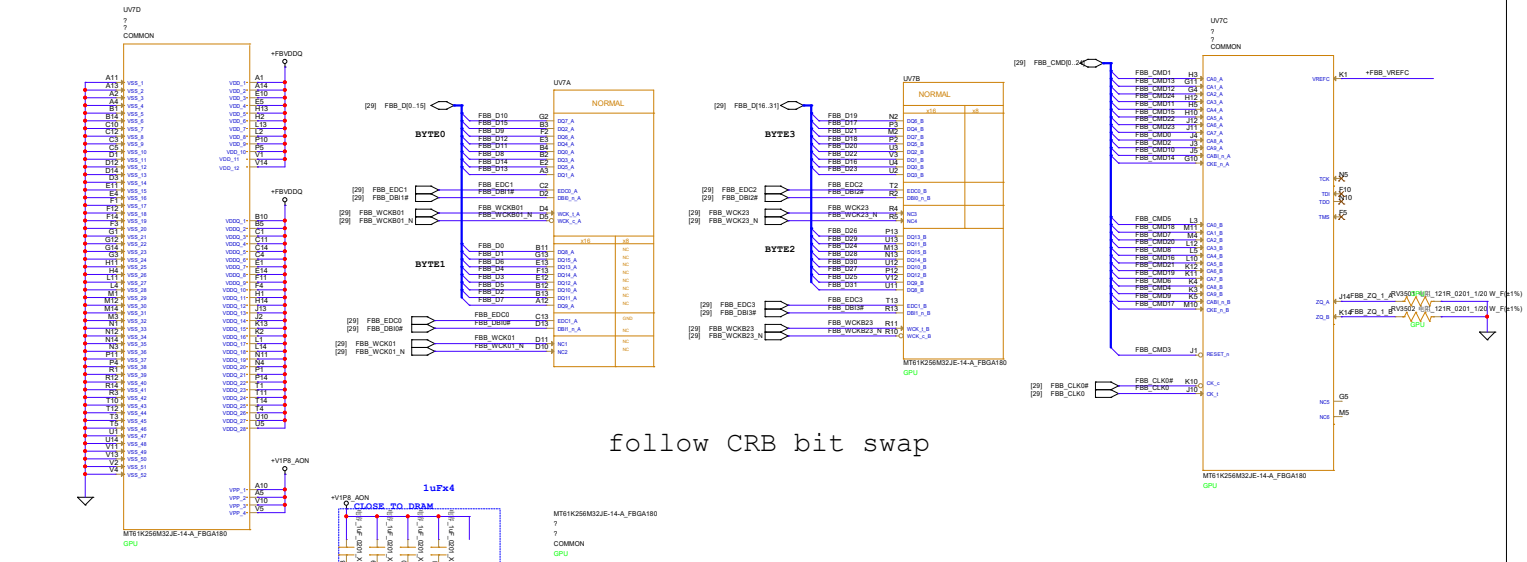


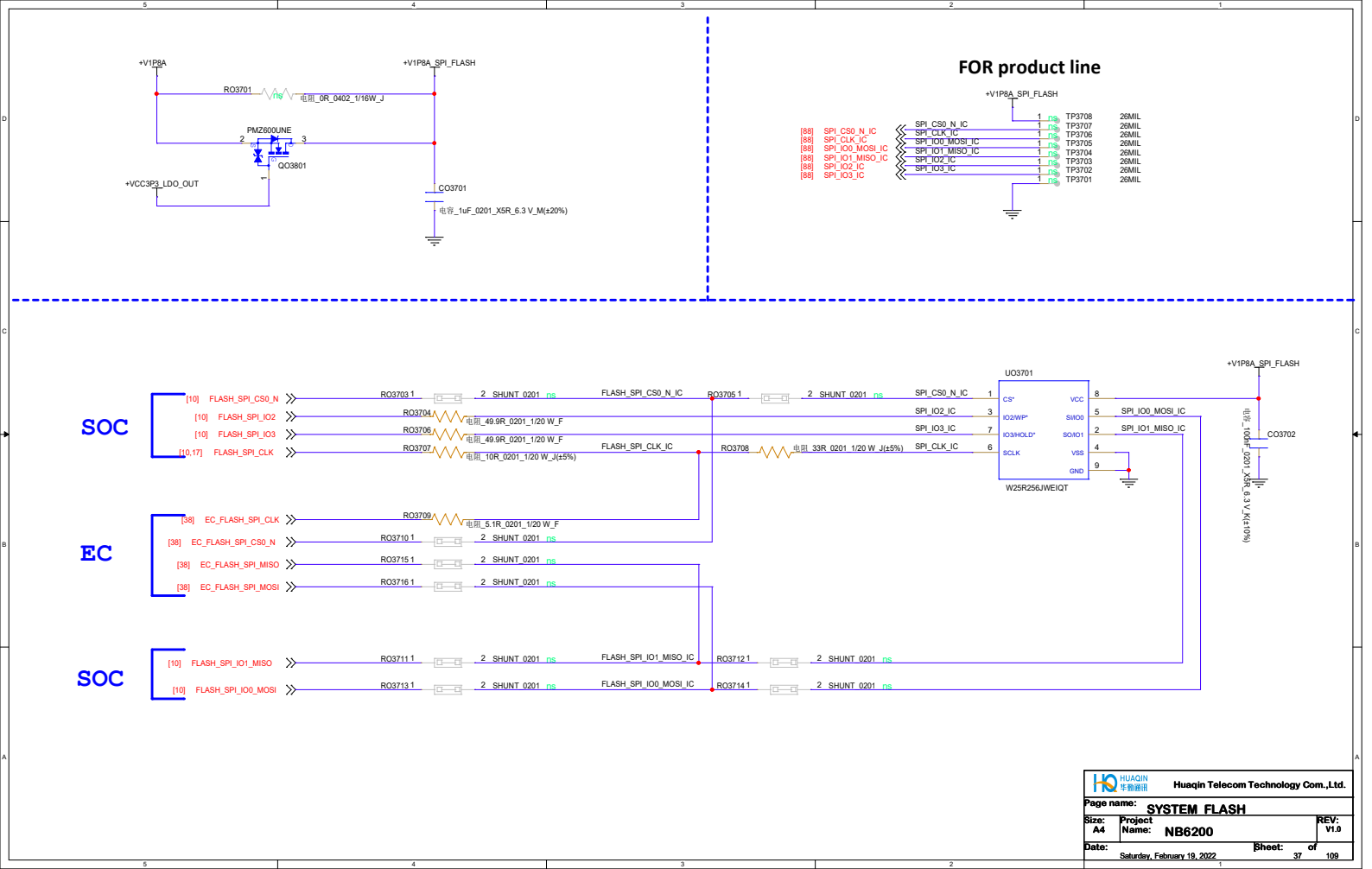
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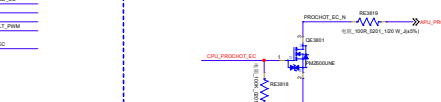
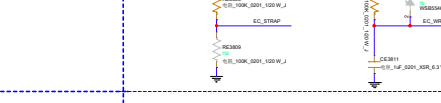
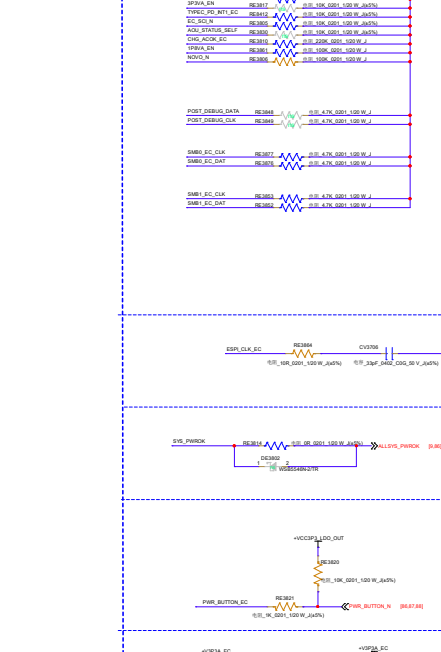
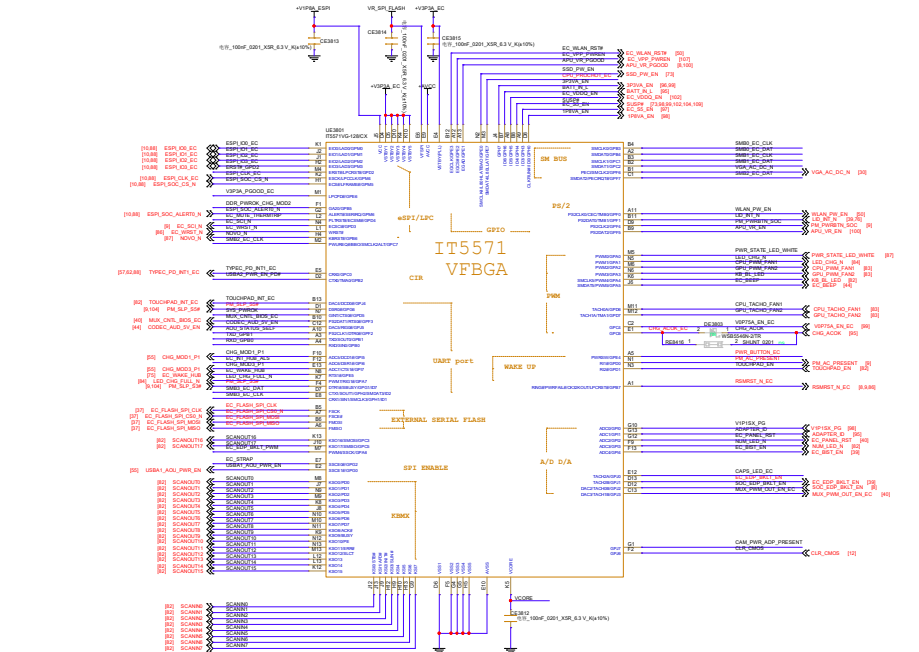
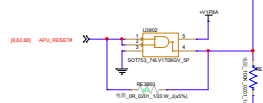
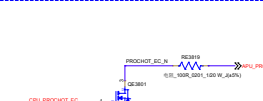
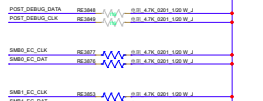
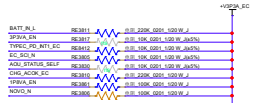
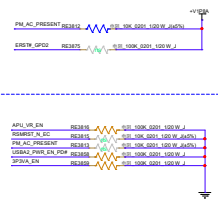
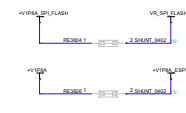
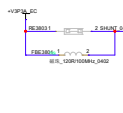


CALIBRATION PIN	COORDS
FB CAL x PS_VDD0	40.200mm
FB CAL x PS_GND	40.200mm
FB CAL x TERM_GND	40.200mm









EC SMBUS Interface

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(EC PU) TSD_GP2 RE3002 1 2 SHUNT 0001 1 POST_DEBUG_DATA [0] Post Debug

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(EC PU) TSD_GP4 RE3004 1 2 SHUNT 0001 1 POST_DEBUG_DATA [0] Post Debug

(EC PU) TSD_GP5 RE3005 1 2 SHUNT 0001 1 POST_DEBUG_DATA [0] Post Debug

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(EC PU) TSD_GP7 RE3007 1 2 SHUNT 0001 1 POST_DEBUG_DATA [0] Post Debug

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(BLK) VDDA_P005 RE3006 1 2 SHUNT 0001 1 VDDA_P005_EC

(BLK) VDDA_P006 RE3007 1 2 SHUNT 0001 1 VDDA_P006_EC

(BLK) VDDA_P007 RE3008 1 2 SHUNT 0001 1 VDDA_P007_EC

(BLK) VDDA_P008 RE3009 1 2 SHUNT 0001 1 VDDA_P008_EC

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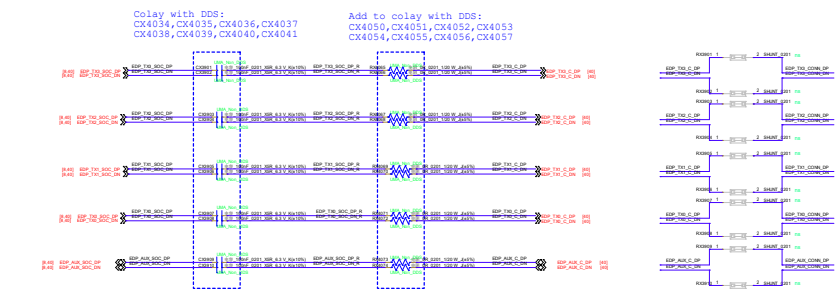
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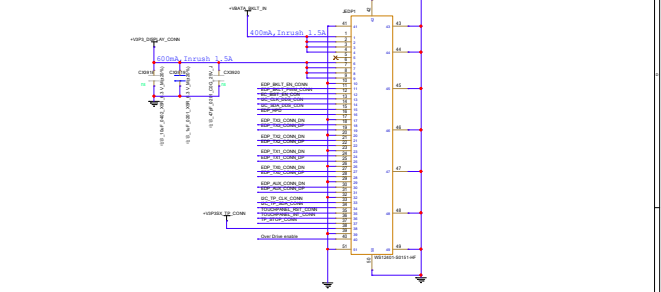
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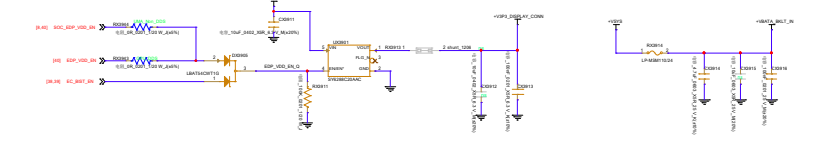
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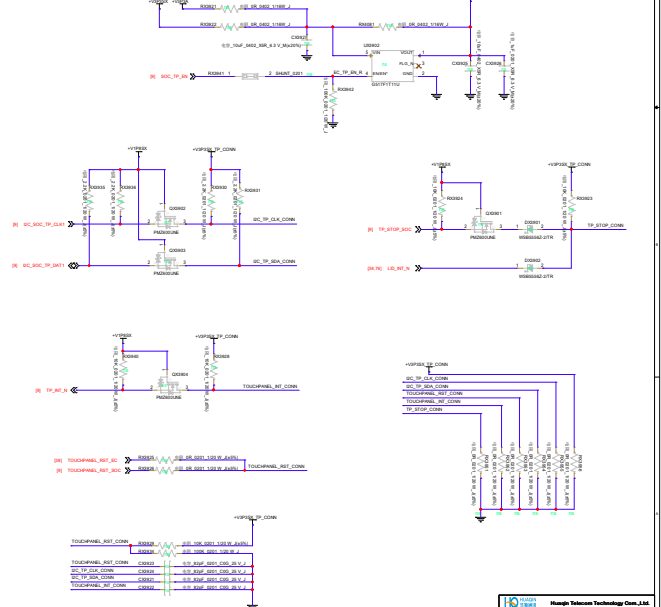
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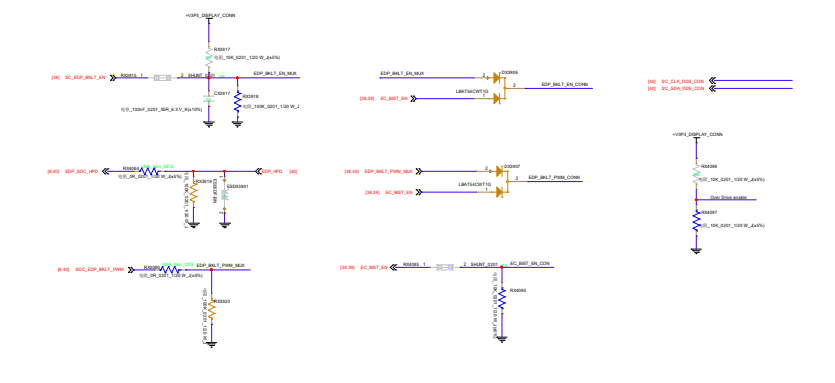
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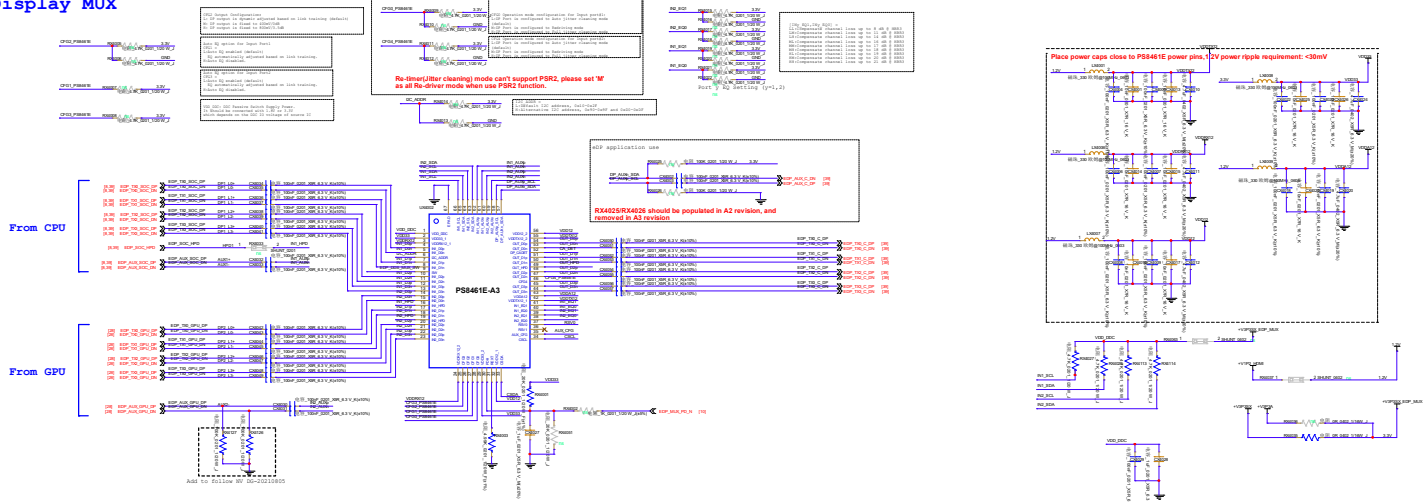
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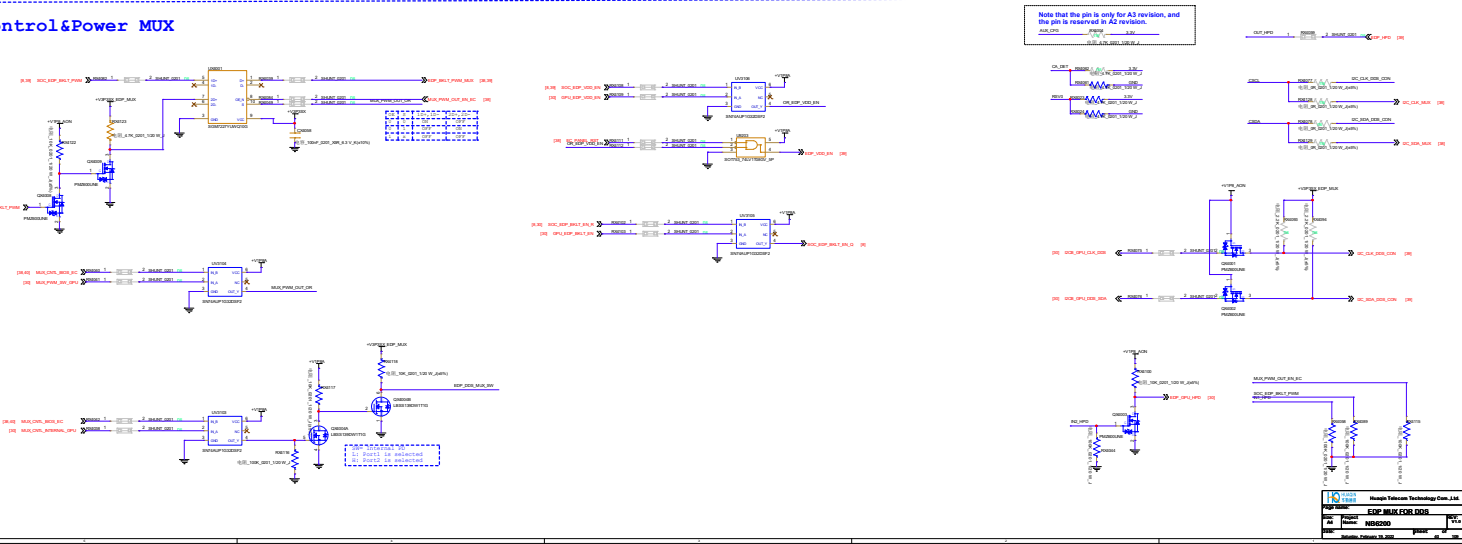
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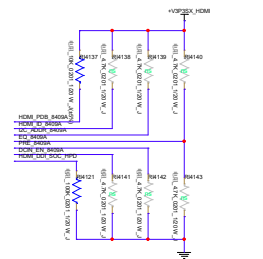
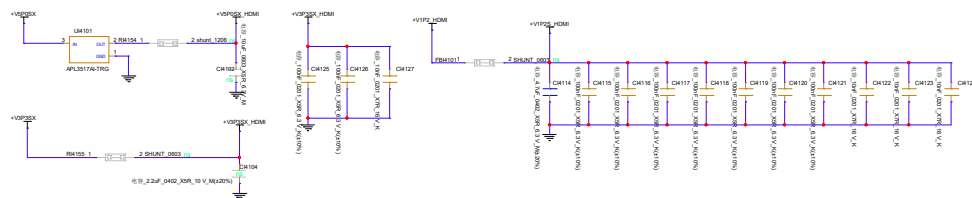
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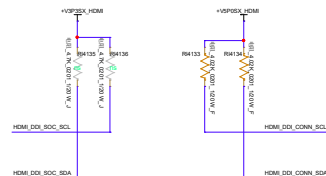
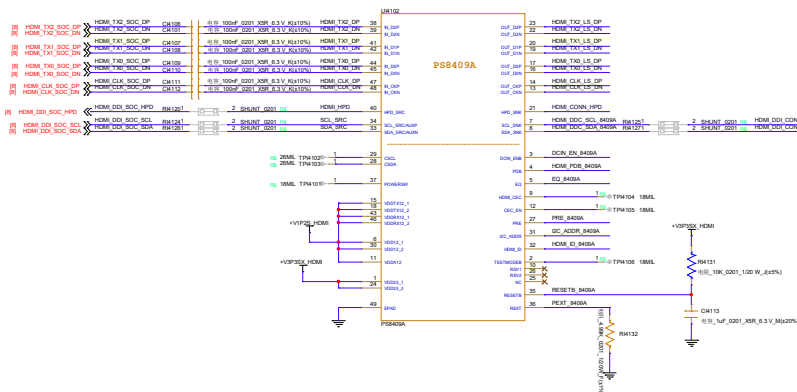
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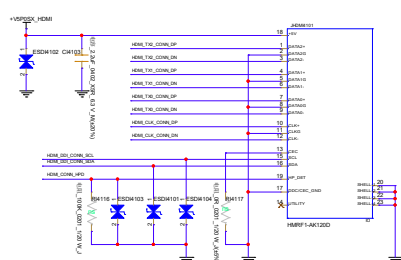
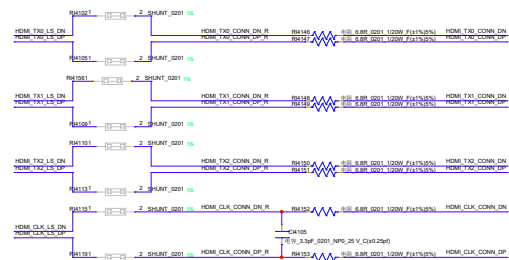
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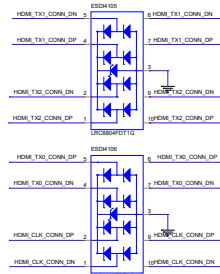
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HDMI CONN




ESD

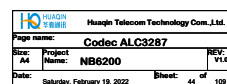


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NB6200

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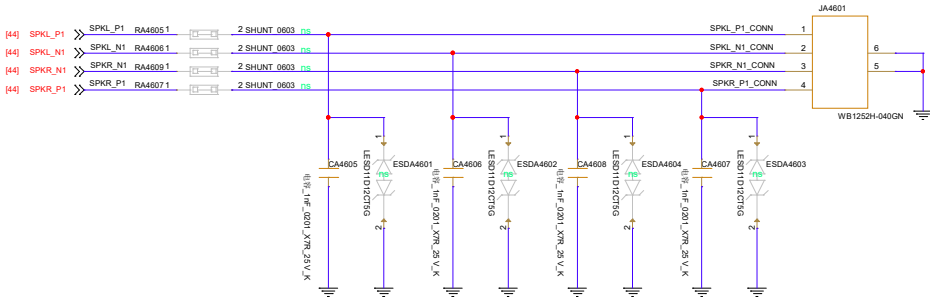
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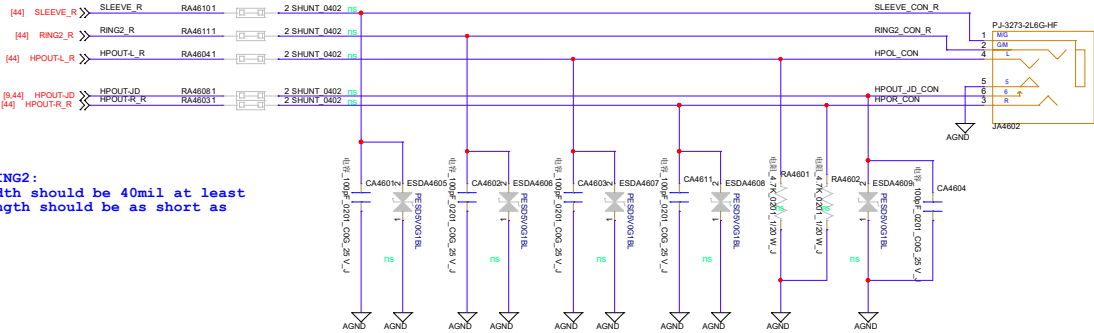
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Speaker Conn



FOR product line			
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SPKL_N1_CONN	1	FTP4607	26MIL
SPKR_P1_CONN	1	FTP4606	26MIL
SPKR_N1_CONN	1	FTP4605	26MIL


Audio Jack




FOR product line			
SLEEVE_CON_R	1	FTP4605	26MIL
HPOL_CON	1	FTP4604	26MIL
HPOL_CON	1	FTP4603	26MIL
HPOL_CON	1	FTP4602	26MIL
RING2_CON_R	1	FTP4601	26MIL

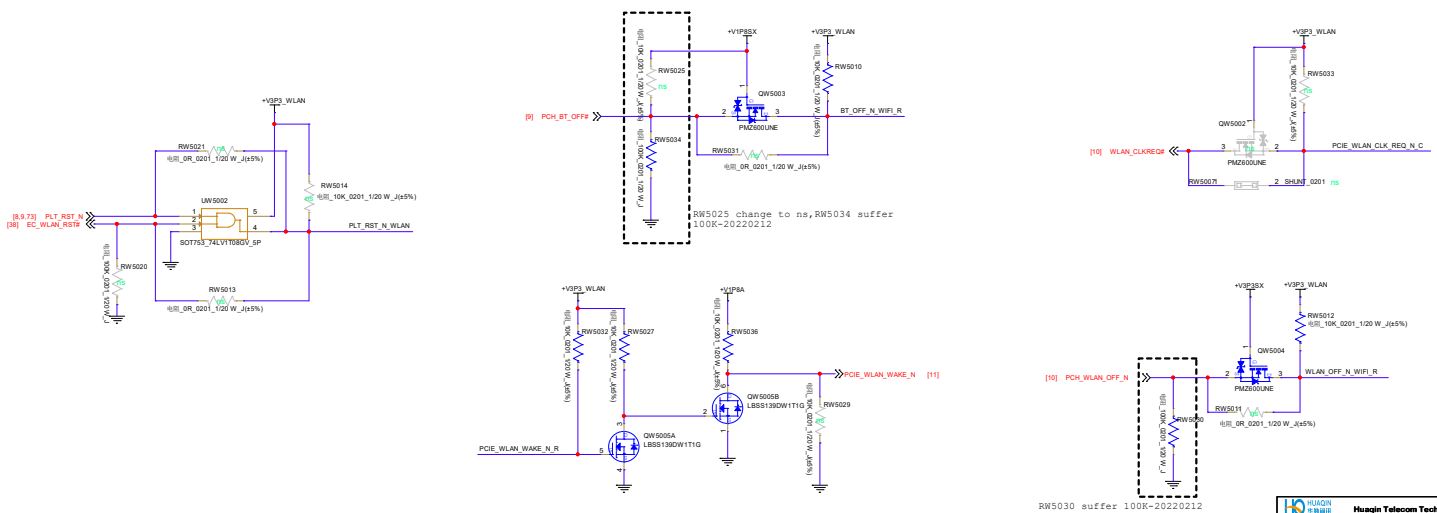
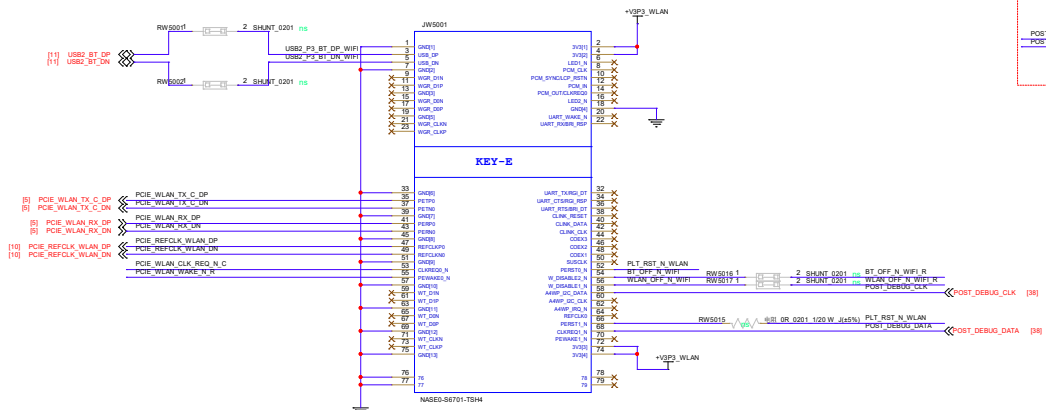
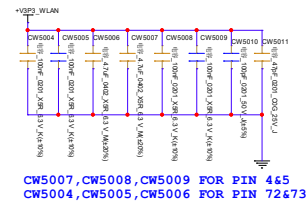
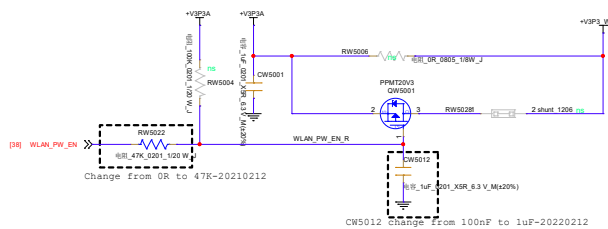
SLEEVE & RING2:
1.Trace width should be 40mil at least
2.Trace length should be as short as possible

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5	4	3	2	1


 HUAQIN 华勤通信		Huaqin Telecom Technology Com.,Ltd.	
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5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

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5	4	3	2	1
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A				
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5	4	3	2	1																
D				D																
C				C																
B				B																
A				A																
5	4	3	2	1																
				<table><tr><td colspan="2"> HUAQIN 平勤通信</td><td colspan="2">Huaqin Telecom Technology Com.,Ltd.</td></tr><tr><td colspan="4">Page name: BLANK</td></tr><tr><td>Size: A4</td><td>Project Name: NB6200</td><td colspan="2">REV: V1.0</td></tr><tr><td>Date: Saturday, February 19, 2022</td><td>Sheet: 52</td><td>of</td><td>109</td></tr></table>	 HUAQIN 平勤通信		Huaqin Telecom Technology Com.,Ltd.		Page name: BLANK				Size: A4	Project Name: NB6200	REV: V1.0		Date: Saturday, February 19, 2022	Sheet: 52	of	109
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D

D

C

C

B

88

A

A



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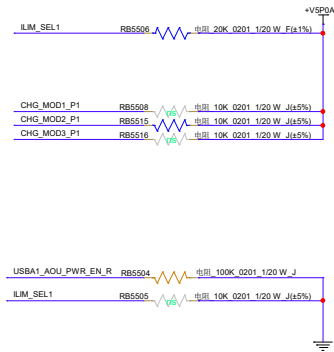
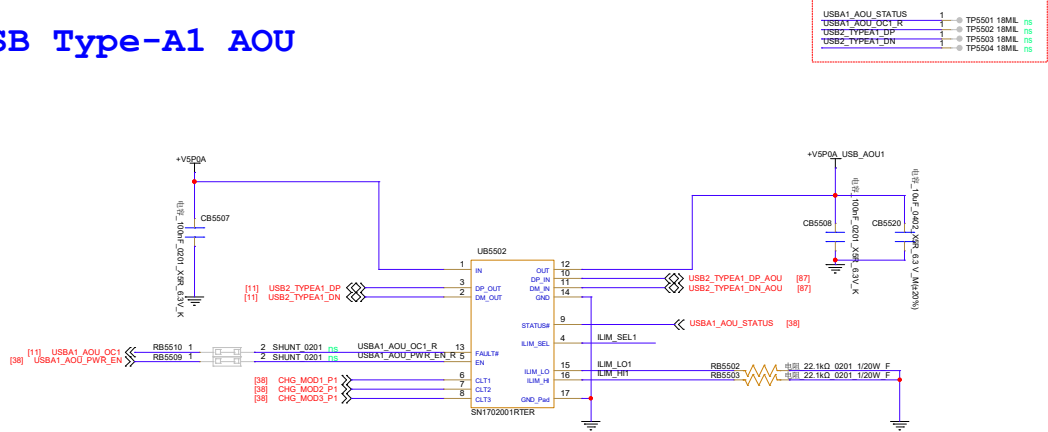
Project Name:	NB6200
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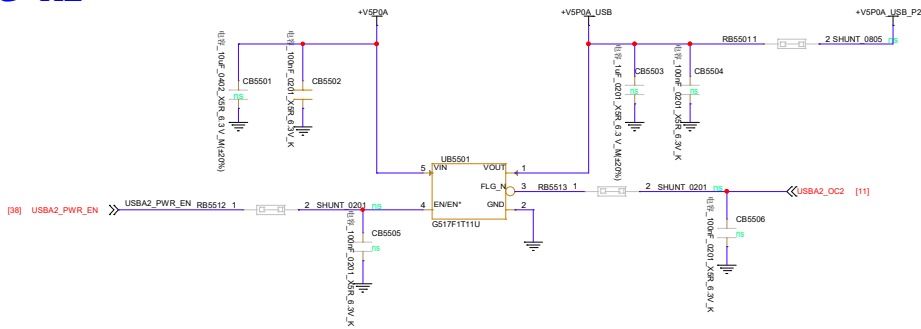
Date: Saturday, February 19, 2022

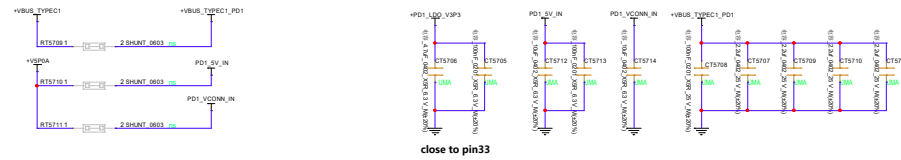
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USB Type-A1 AOU

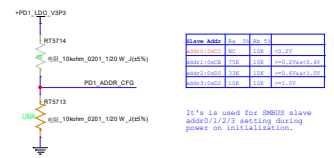


USB Type-A2





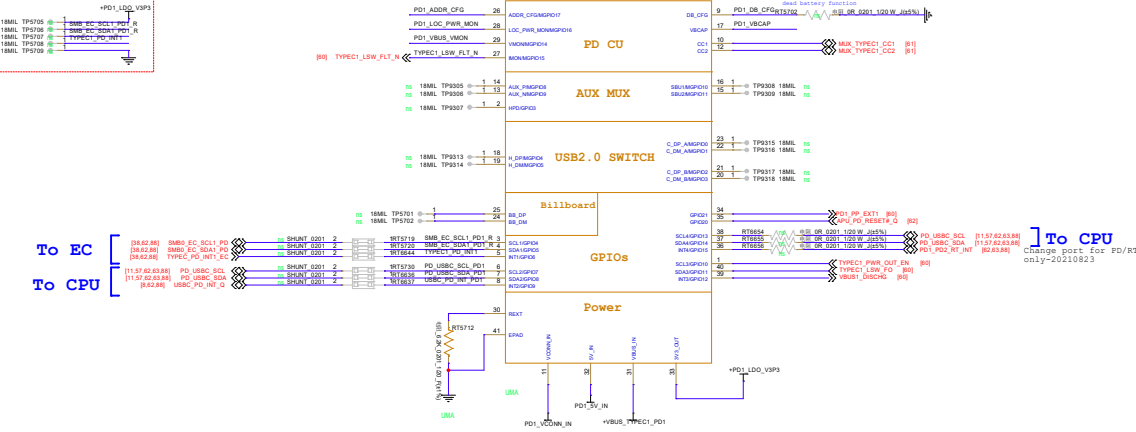
I2C Address: 0xCC



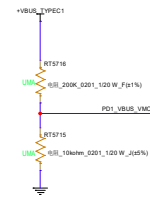
FOR PD FW Update



RTS5457V-GR

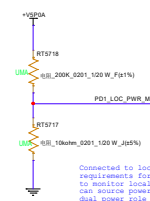


VBUS Voltage/Current Monitor




1. IMON is connected to CSA output for VBUS current monitor.
2. Also can be connected to VBUS PMIC over current detect pin for CCM Function.
3. The maximum voltage on IMON pin should be less 3.6V

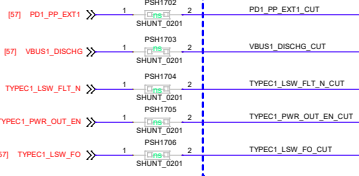
Local Power Voltage Monitor



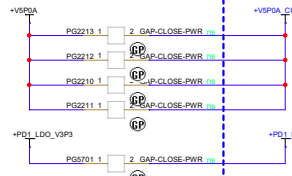
5		4		3		2		1	
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Size: A4		Project Name: NB6200						REV: V1.0	
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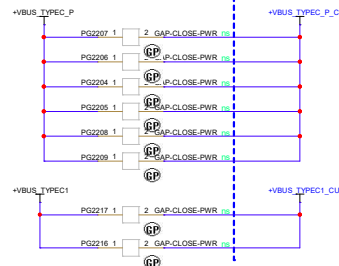
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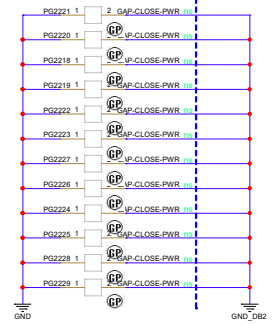
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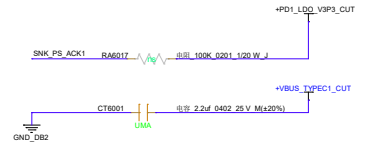
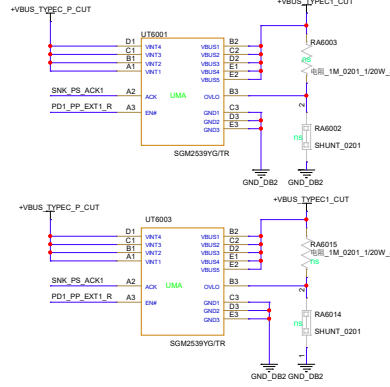
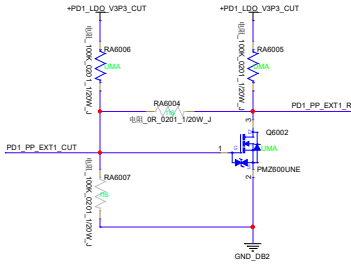
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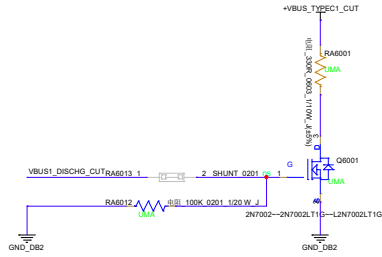
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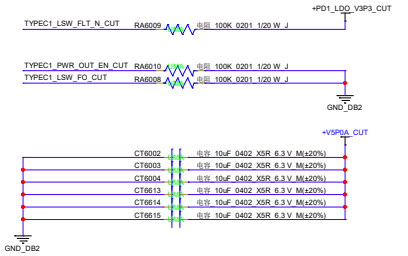
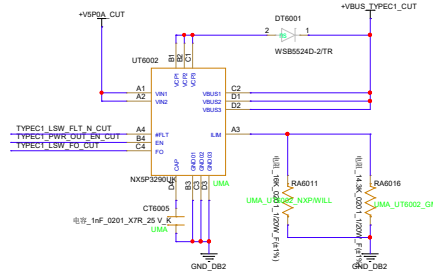
PORT1 SINK



VBUS1 DISCHARGE



PORT1 SOURCE





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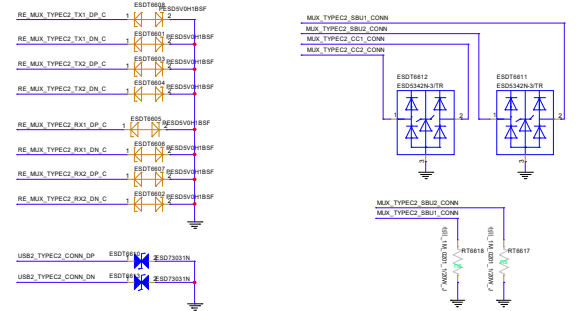
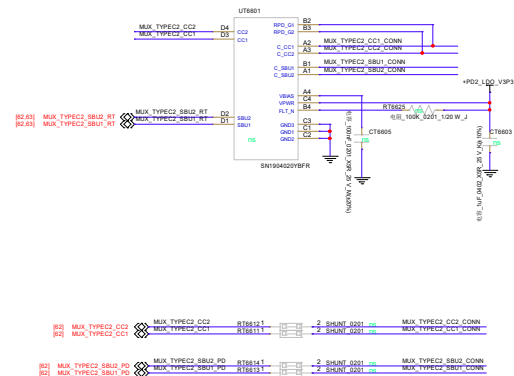
PORT2 SINK

VBUS2 DISCHARGE

PORT2 SOURCE

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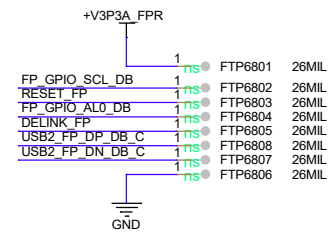
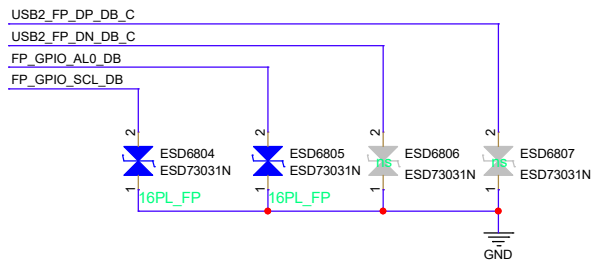
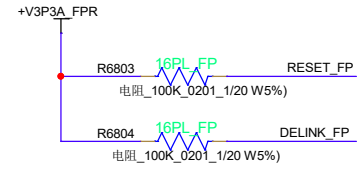
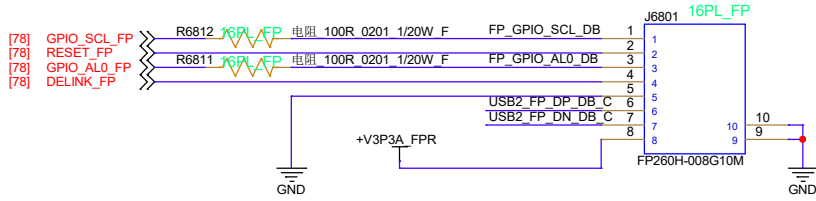
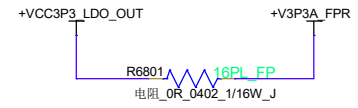
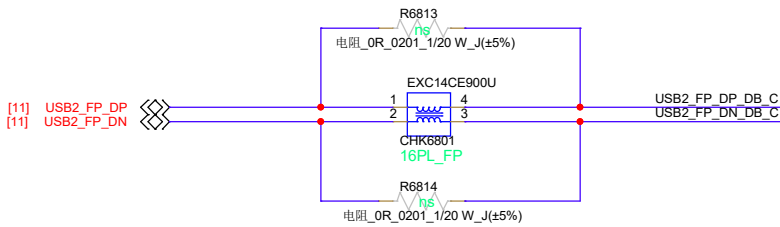
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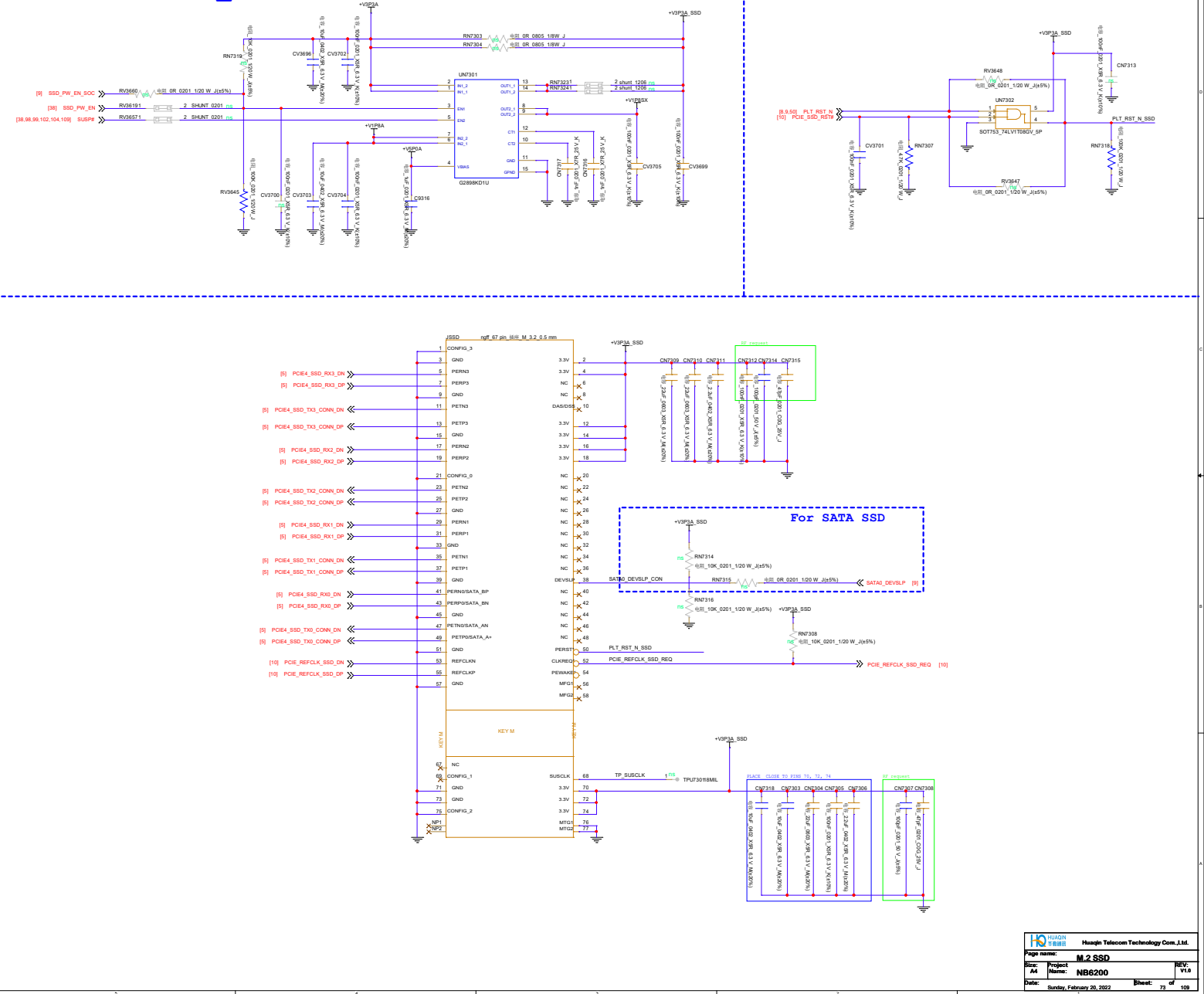
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V1P8SX & +V3P3A_SSD





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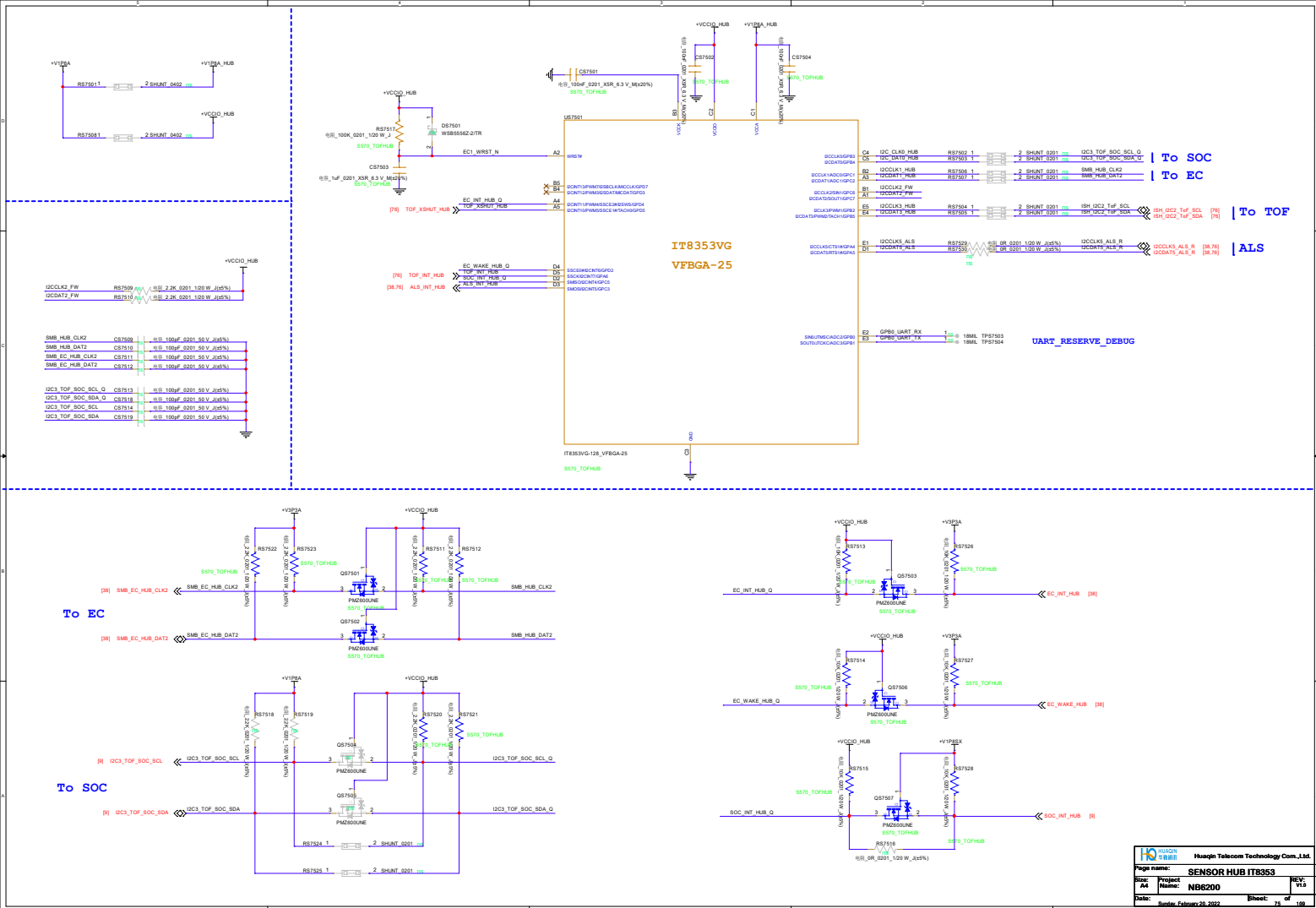
Date:

Saturday, February 19, 2022

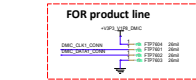
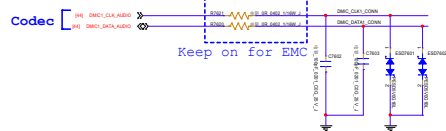
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of

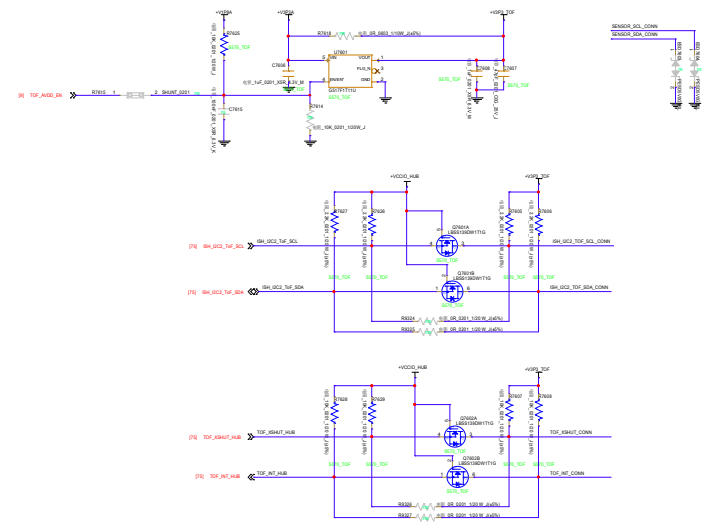
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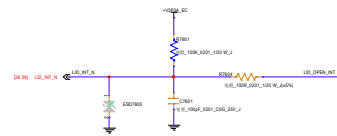
DMIC



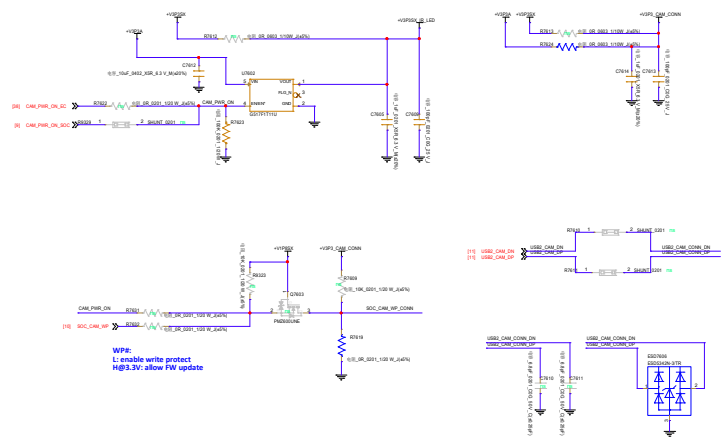
ToF Sensor



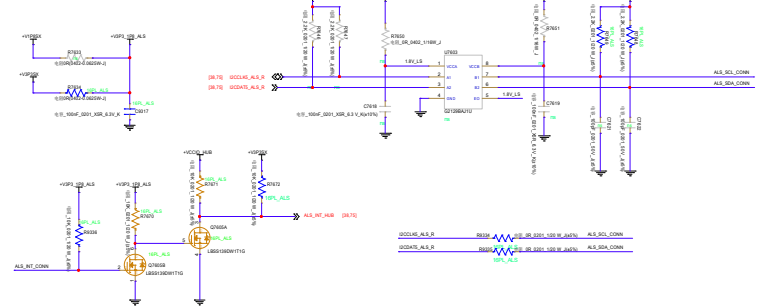
Hall Sensor



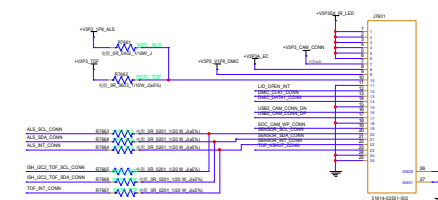
Camera



ALS



TOF/ IR CAM/ DMIC CONN





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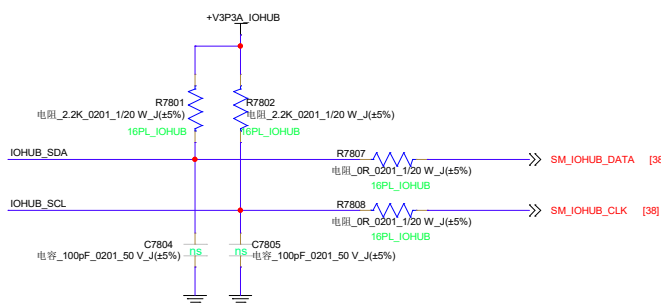
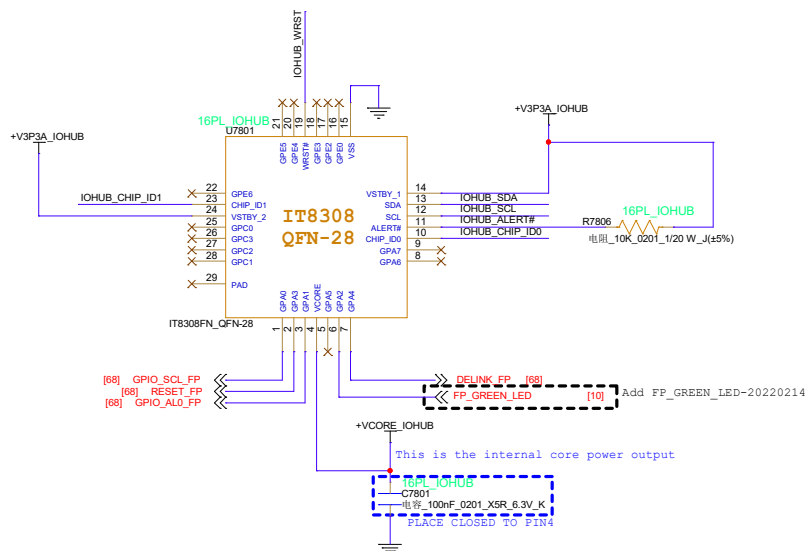
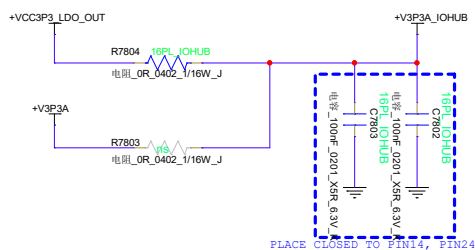
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Command	SAD[7:3], ID[1]	ID[0], ID[0]	R/W	=	SAD+R/W
Read	01000	0	0	1	01000001 (41h)
Write	01000	0	1	0	01000000 (40h)
Read	01000	0	1	1	01000011 (43h)
Write	01000	0	1	0	01000010 (42h)
Read	01000	1	0	1	01000101 (45h)
Write	01000	1	0	0	01000100 (44h)
Read	01000	1	1	0	01000111 (47h)
Write	01000	1	1	0	01000110 (46h)



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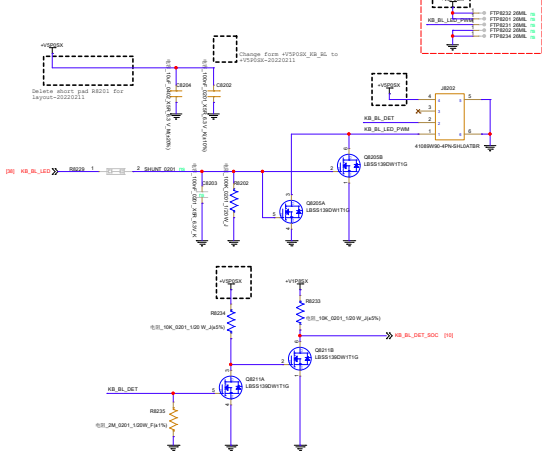
Saturday, February 19, 2022

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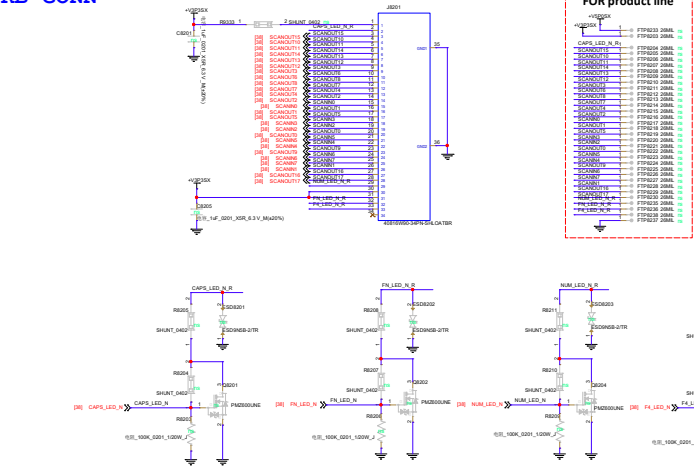
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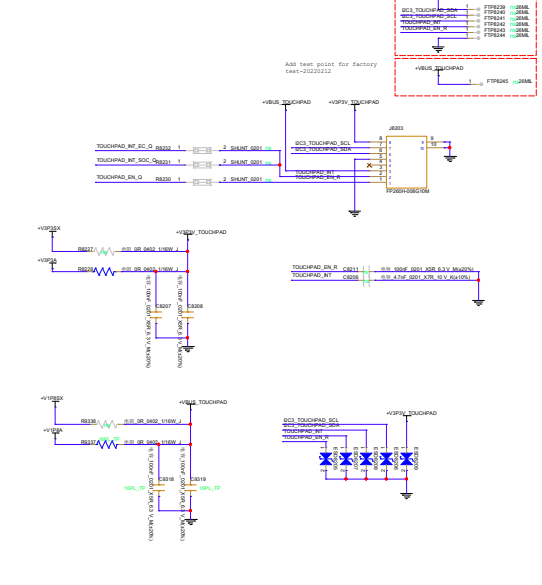
KB Backlight



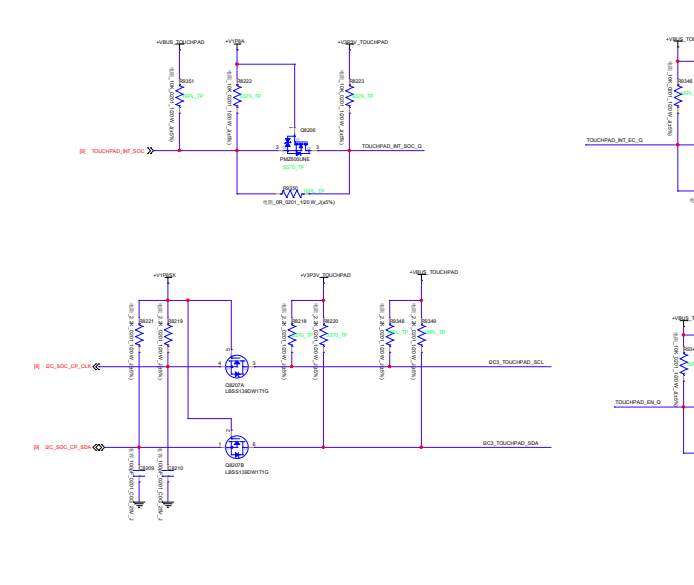
KB CONN

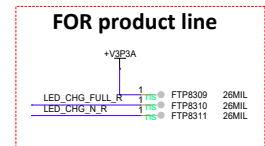



Click Pad CONN



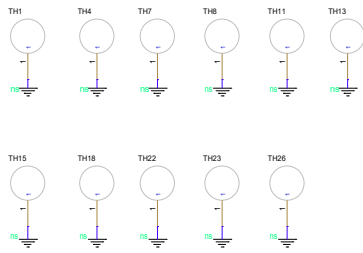
Click Pad Signal



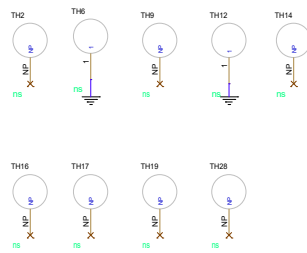


 HUAQIN 华勤通讯	Huaqin Telecom Technology Co., Ltd.	
	Page name: CHARGE LED	
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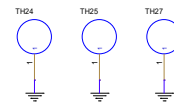
MB LOCKUP HOLD



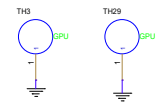
MB LOCATION HOLD



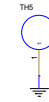
CPU NUT



GPU NUT



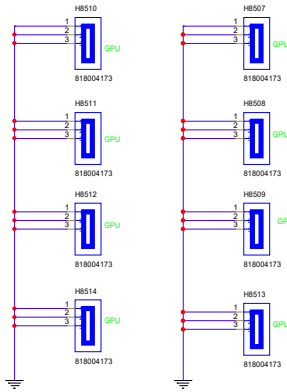
WIFI NUT



SSD NUT



GDDR Clip



CPU shielding



GPU shielding



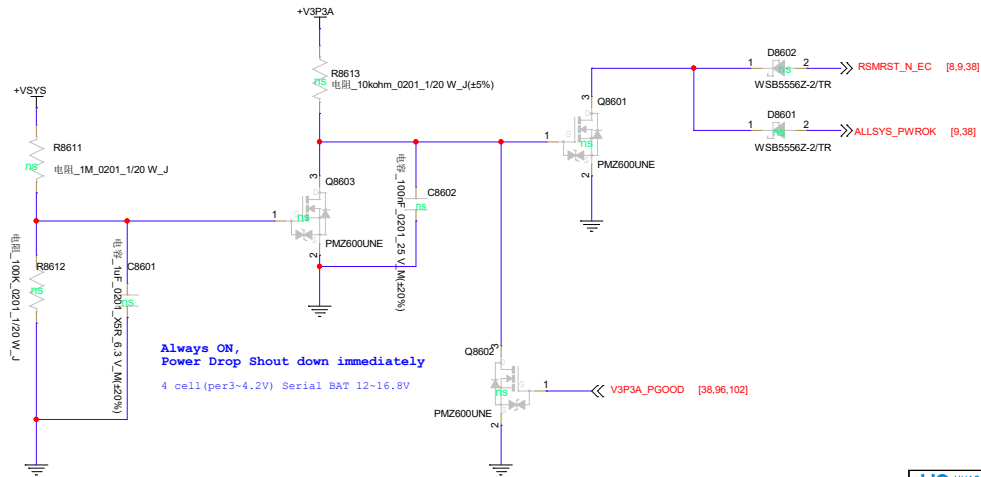
Delete when creat net list



Delete when creat net list

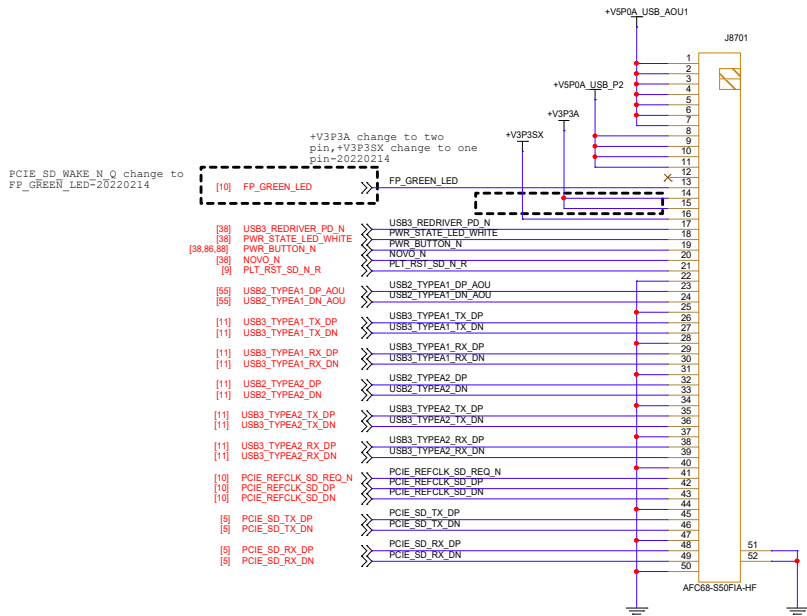
Add GPU shielding-20211013

Abnormal Shutdown Reset



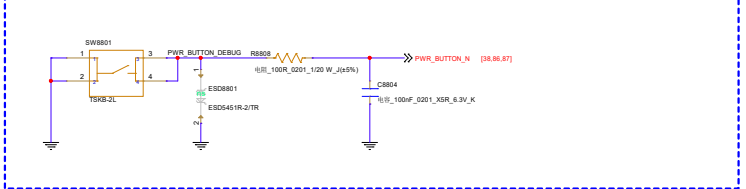
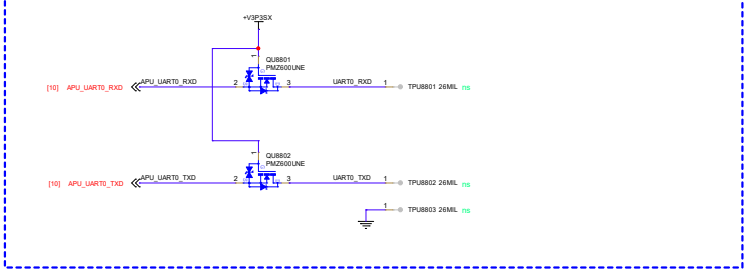
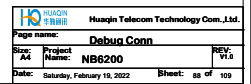
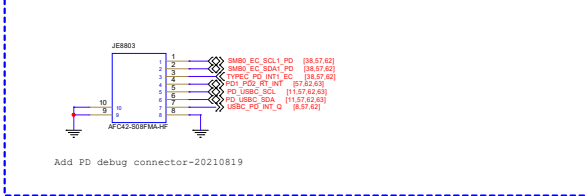
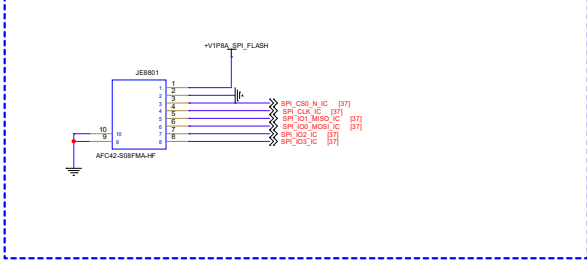
Always ON,
Power Drop Shout down immediately

4 cell(per3~4.2V) Serial BAT 12~16.8V



HDT+ Connector for Debug only

Cap close to JHD1.9

[illegible]

PCIE_SD_WAKE_N_Q change to
FP_GREEN_LED=20220214

[92] FP_GREEN_LED_DB

+V3P3A_DB change to two
pin,+V3P3SX_DB change to one
pin=20220214

FP_GREEN_LED_DB

- [90,91] USB3_REDRIIVER_PD_N_DB
[93] PWR_STATE_LED_WHITE_DB
[93] PWR_BUTTON_N_DB
[93] NOVO_N_DB
[92] FLT_RST_SD_N_R_DB
[90] USB2_TYPEA1_DP_AOU_DB
[90] USB2_TYPEA1_DN_AOU_DB
[90] USB3_TYPEA1_TX_DP_DB
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- USB3_REDRIIVER_PD_N_DB
PWR_STATE_LED_WHITE_DB
PWR_BUTTON_N_DB
NOVO_N_DB
FLT_RST_SD_N_R_DB
USB2_TYPEA1_DP_AOU_DB
USB2_TYPEA1_DN_AOU_DB
USB3_TYPEA1_TX_DP_DB
USB3_TYPEA1_TX_DN_DB
USB3_TYPEA1_RX_DP_DB
USB3_TYPEA1_RX_DN_DB
USB2_TYPEA2_DP_DB
USB2_TYPEA2_DN_DB
USB3_TYPEA2_TX_DP_DB
USB3_TYPEA2_TX_DN_DB
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USB3_TYPEA2_RX_DN_DB
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PCIE_REFCLK_SD_DP_DB
PCIE_REFCLK_SD_DN_DB
PCIE_SD_TX_DP_DB
PCIE_SD_TX_DN_DB
PCIE_SD_RX_DP_DB
PCIE_SD_RX_DN_DB

+VSP0A_USB_AOU1_DB

+VSP0A_USB_P2_DB

+V3P3A_DB

+V3P3SX_DB

AFC68-S50F1A-HF

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2

1

GND_DB1

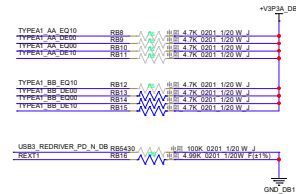
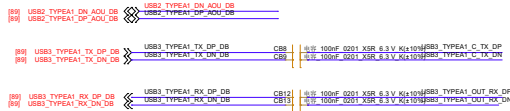
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GND_DB1

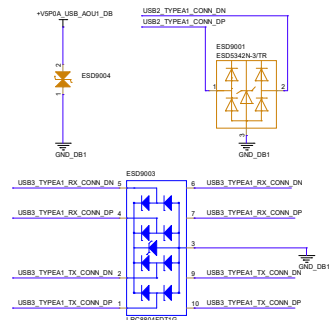
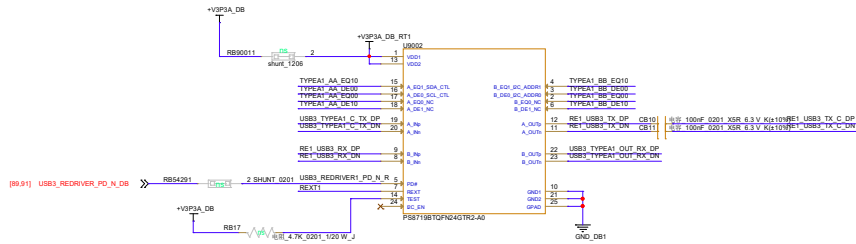
 HUAQIN 华勤通信		Huaqin Telecom Technology Com.,Ltd.	
Page name: DB TO MB CONN			
Size: A4	Project Name: NB6200	REV: V1.0	
Date: Sunday, February 20, 2022	Sheet: 89 of 109		

From CPU

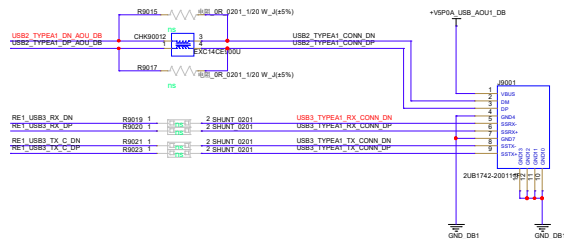
To CPU



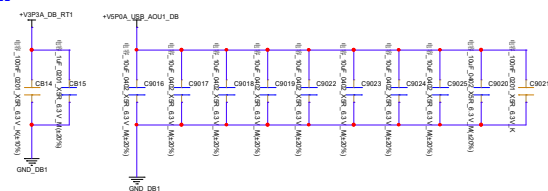
USB Re-driver



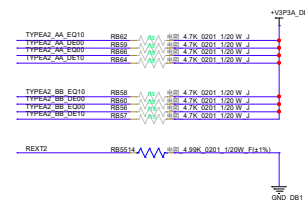
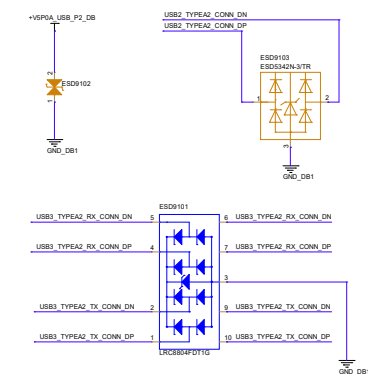
USB Connector



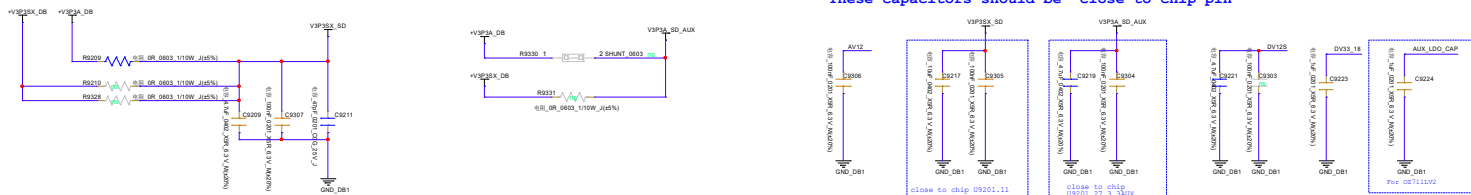
Close to Conn



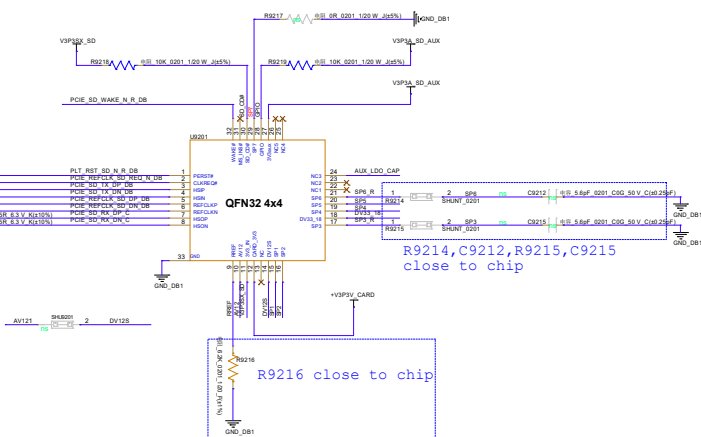
To CPU

[illegible][illegible]

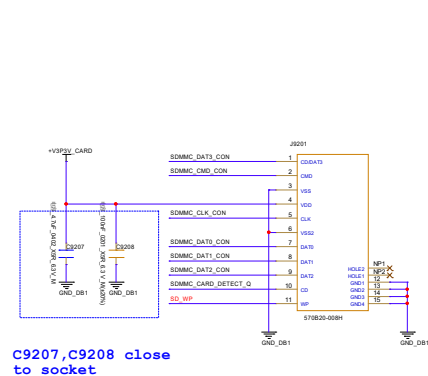
These capacitors should be close to chip pin



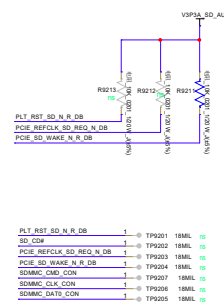
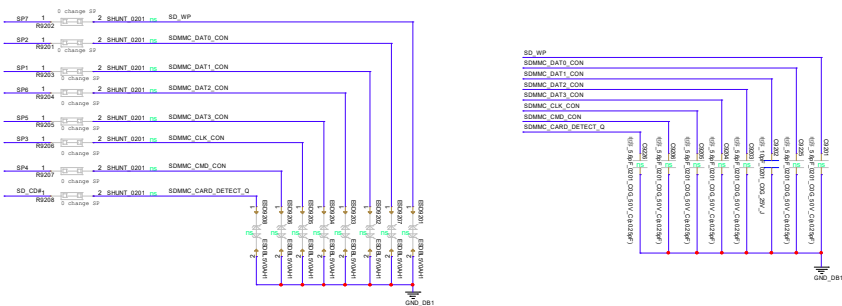
SD Card Controller



SD Card Connector

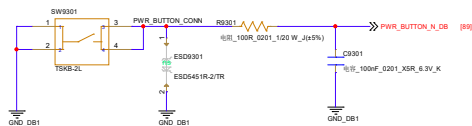


SD Signal



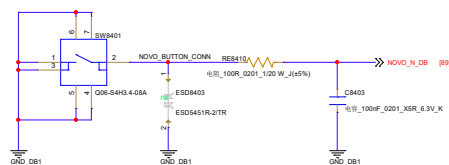
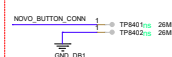
Power BUTTON

FOR product line



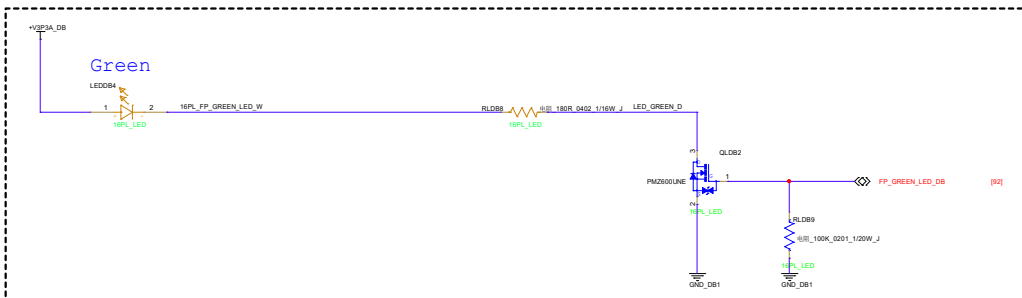
NOVO BUTTON

FOR product line



Finger Print green LED

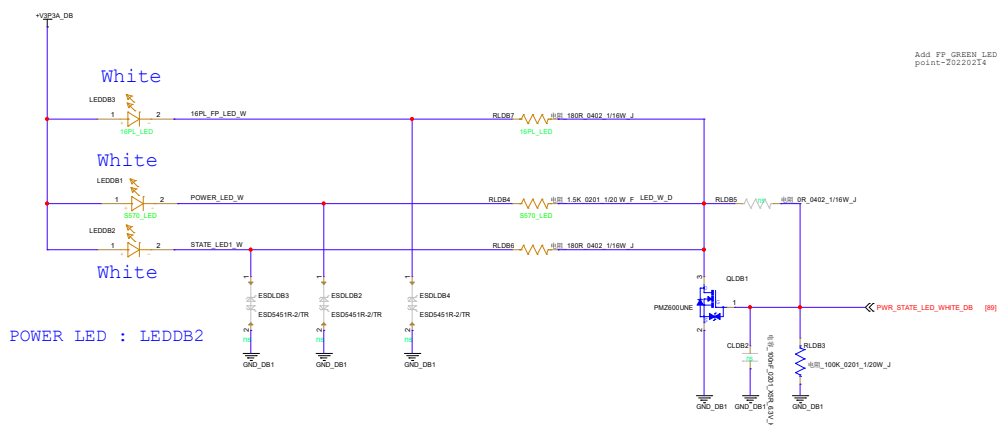
Add FP_GREEN_LED-20220214



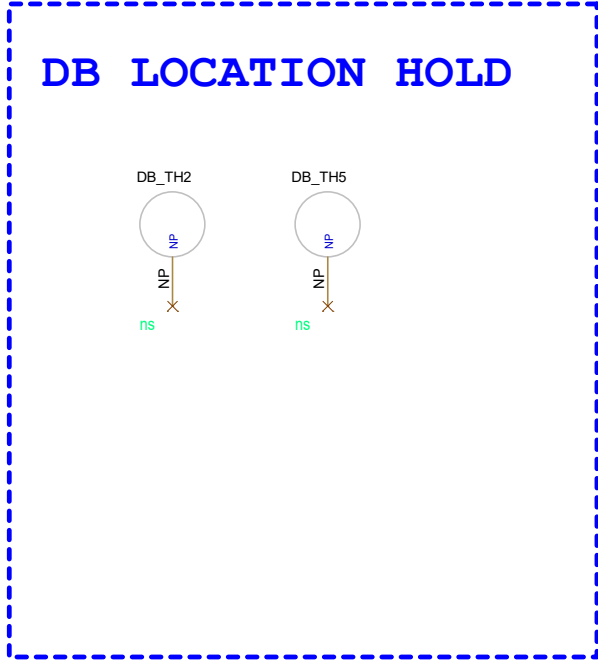
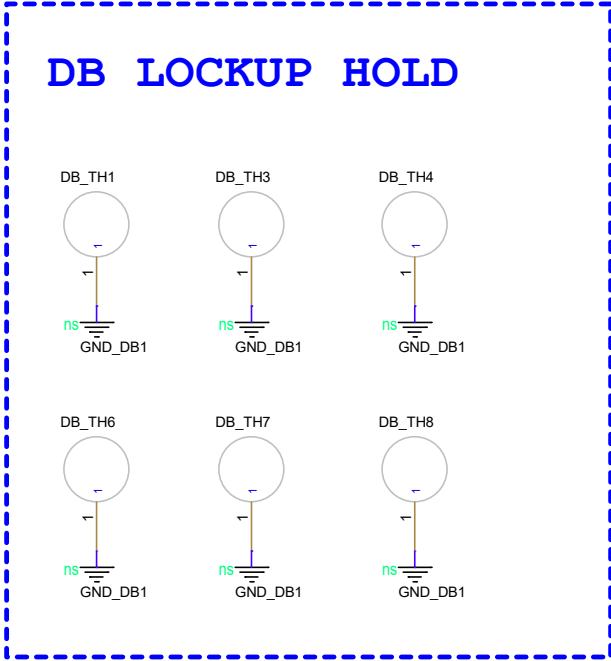
Power states LED


FOR product line

Add FP_GREEN_LED test point-20220214



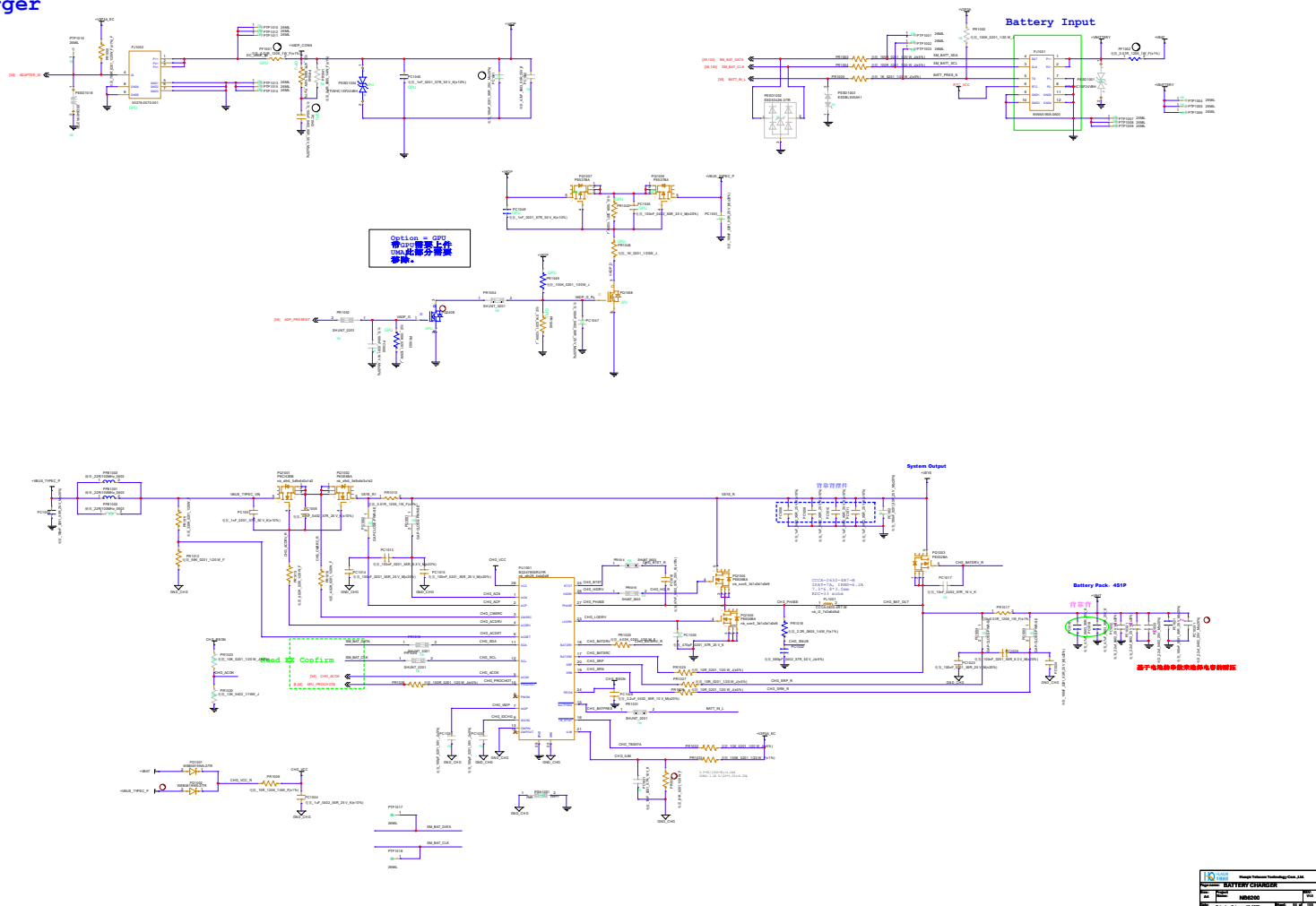
HUAQIN HUAQIN HUAQIN			Huagun Telecom Technology Com., Ltd.		
Page name:			DB_BTN/NOVOLED		
Size:	Project	Name:	REV:		
A4		NB6200	V1.0		
Date:	Sunday, February 20, 2022		Sheet:	81	109

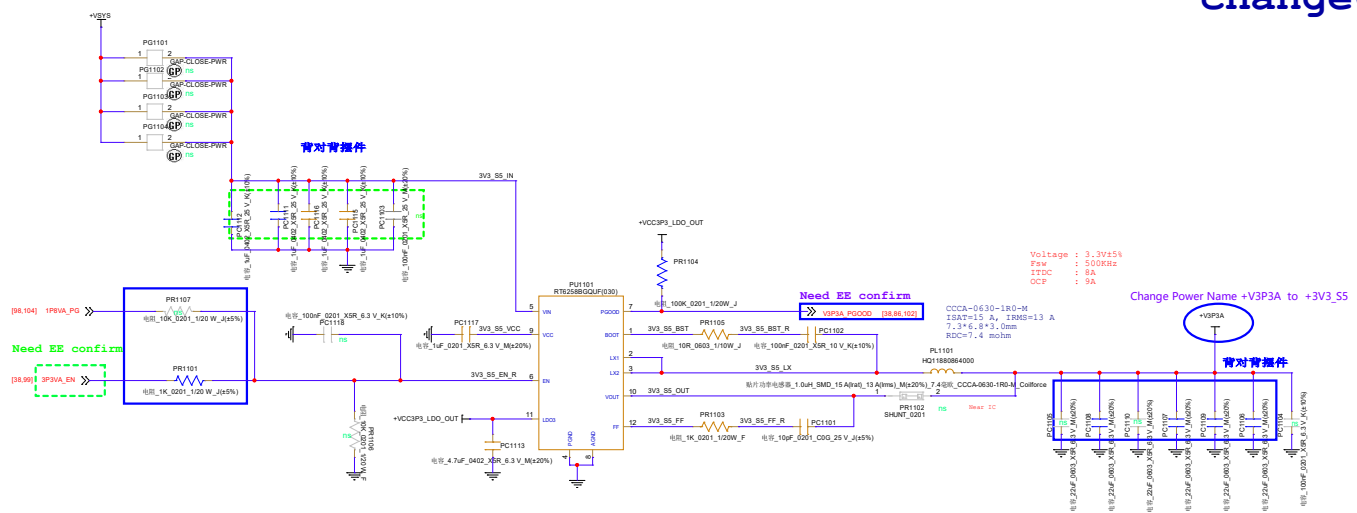


		Huaqin Telecom Technology Com.,Ltd.	
Page name: DB HOLD&MARK			
Size: A4	Project Name: NB6200	REV: V1.0	
Date: Saturday, February 19, 2022	Sheet: 94	of	109

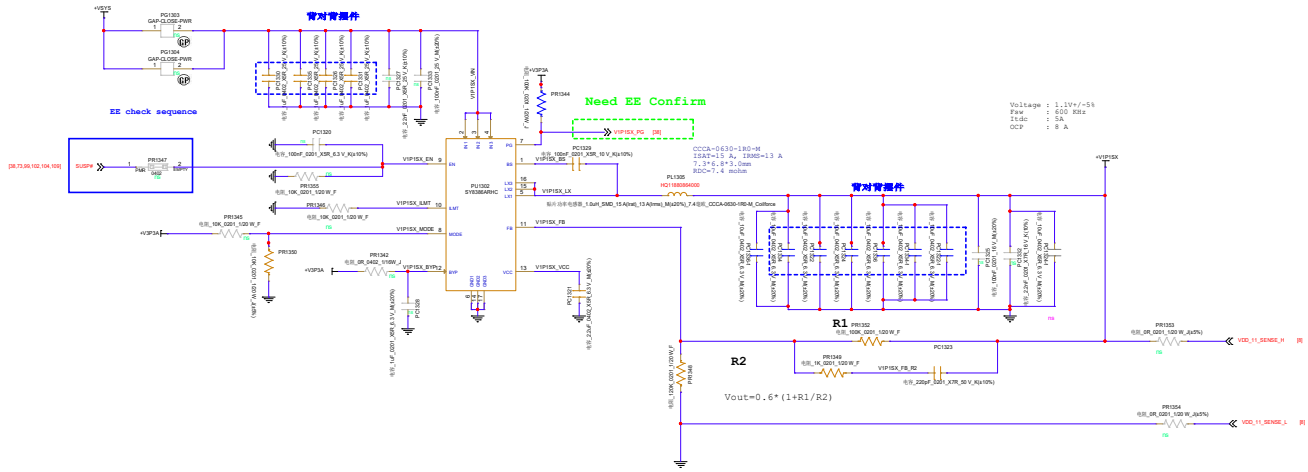
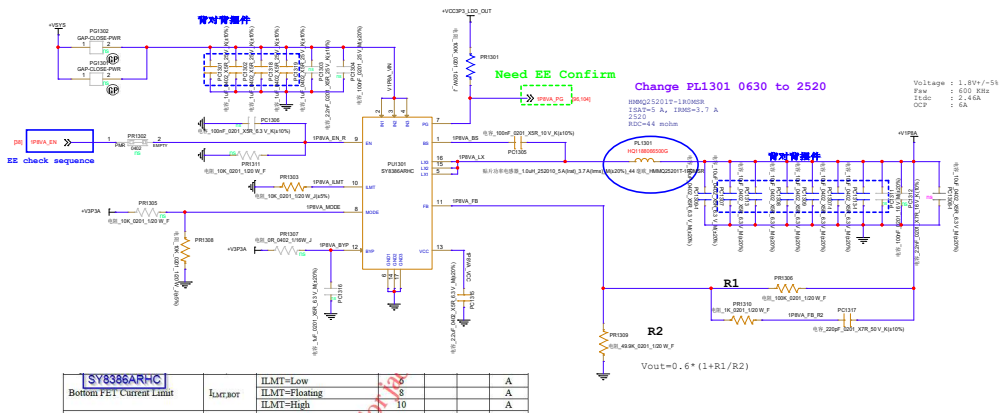
ID PIN Configure						
适配器功能	135W Adapter	90W Adapter	65W Adapter	45W Adapter		
ID PIN Voltage	ID<3.1V	3V>ID>1.663V	1.618V>ID>1.172V	1.134V>ID>0.693V	0.663V>ID>0.234V	0.2V>ID>0V
万针状态	OUT	IN	IN	IN	IN	OUT
INPUT CURRENT SPLITTING (93H)	0A	6.27A	4.18A	3.02A	2.09A	0A

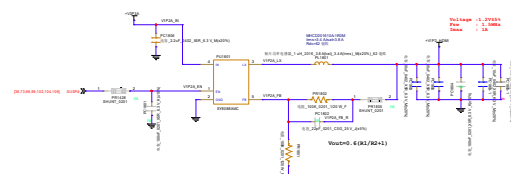
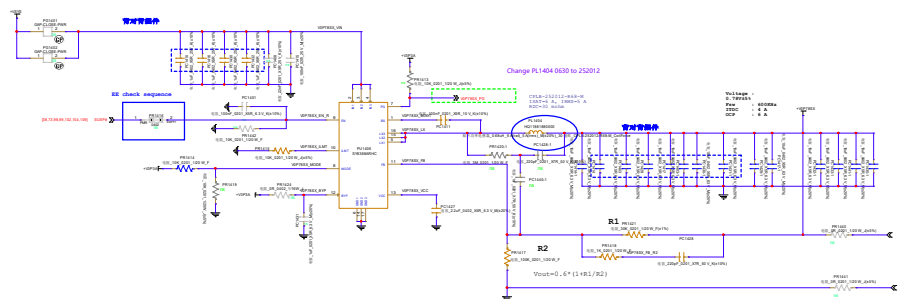
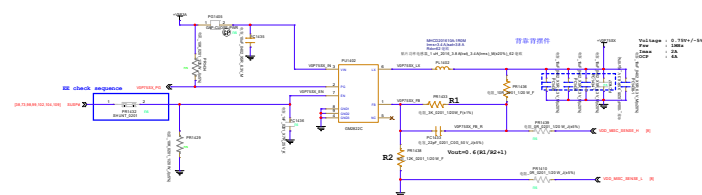
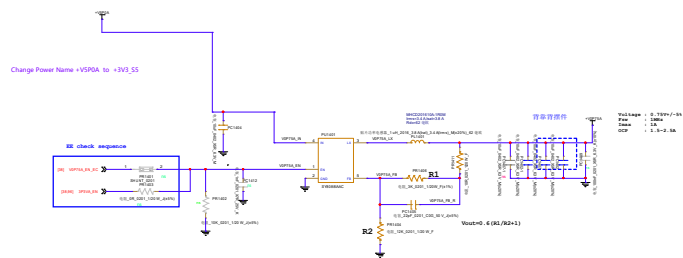
Charger

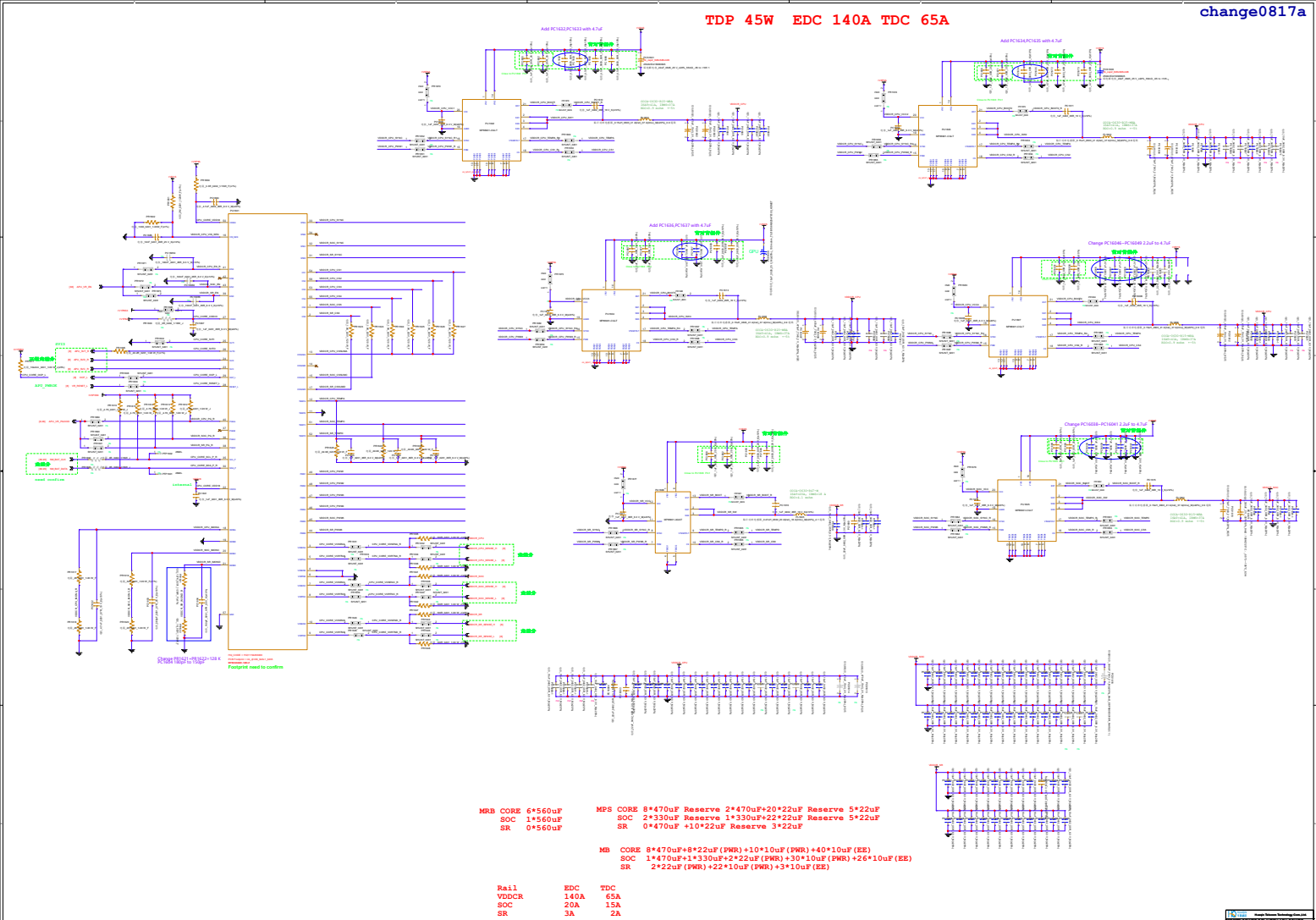




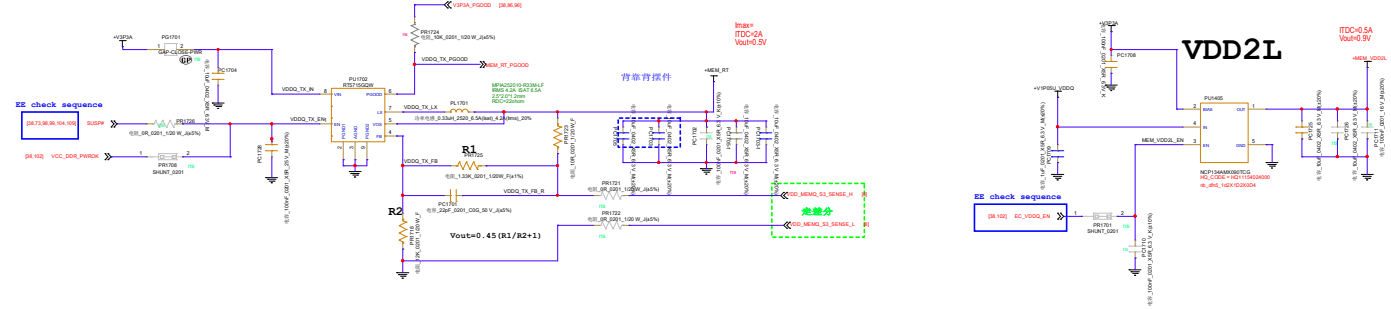
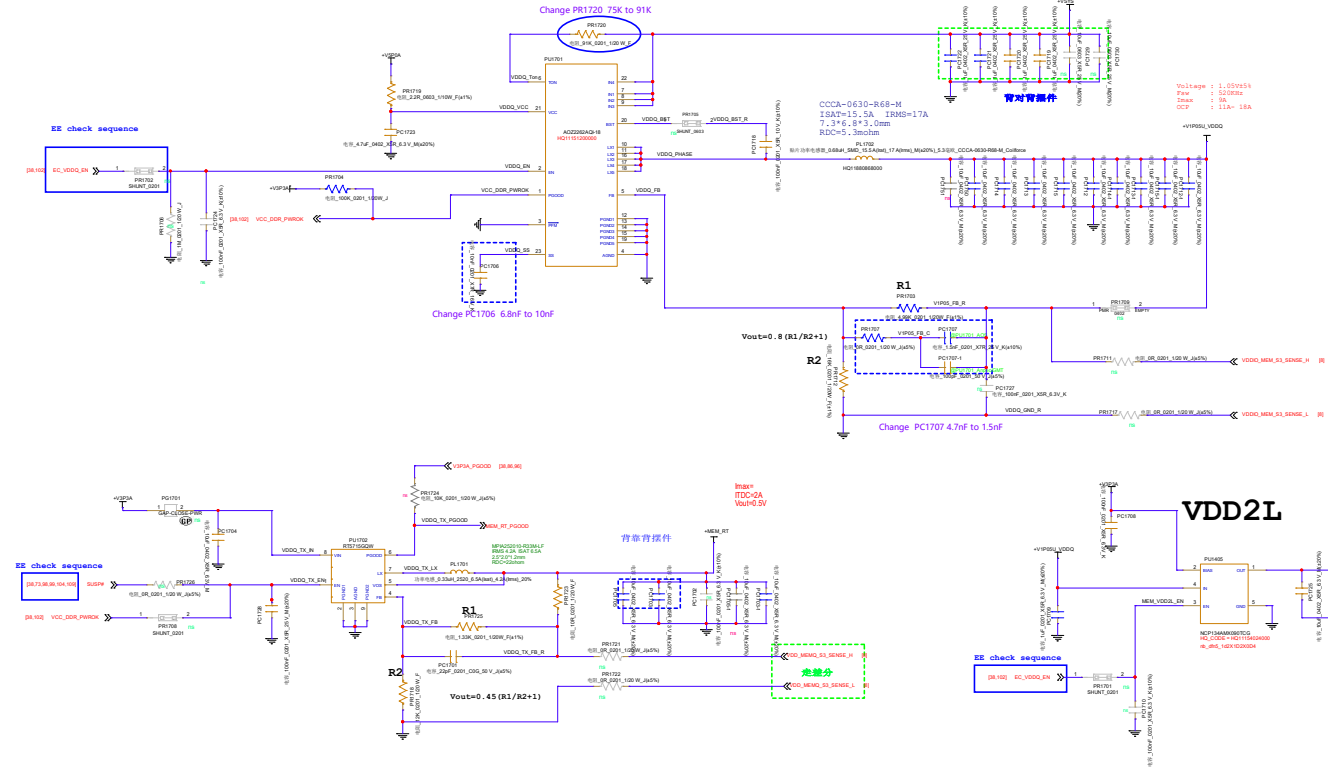




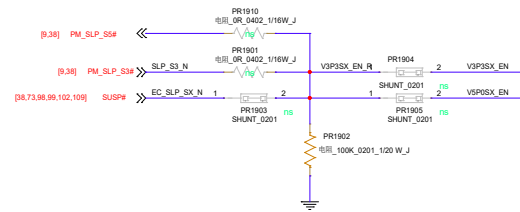




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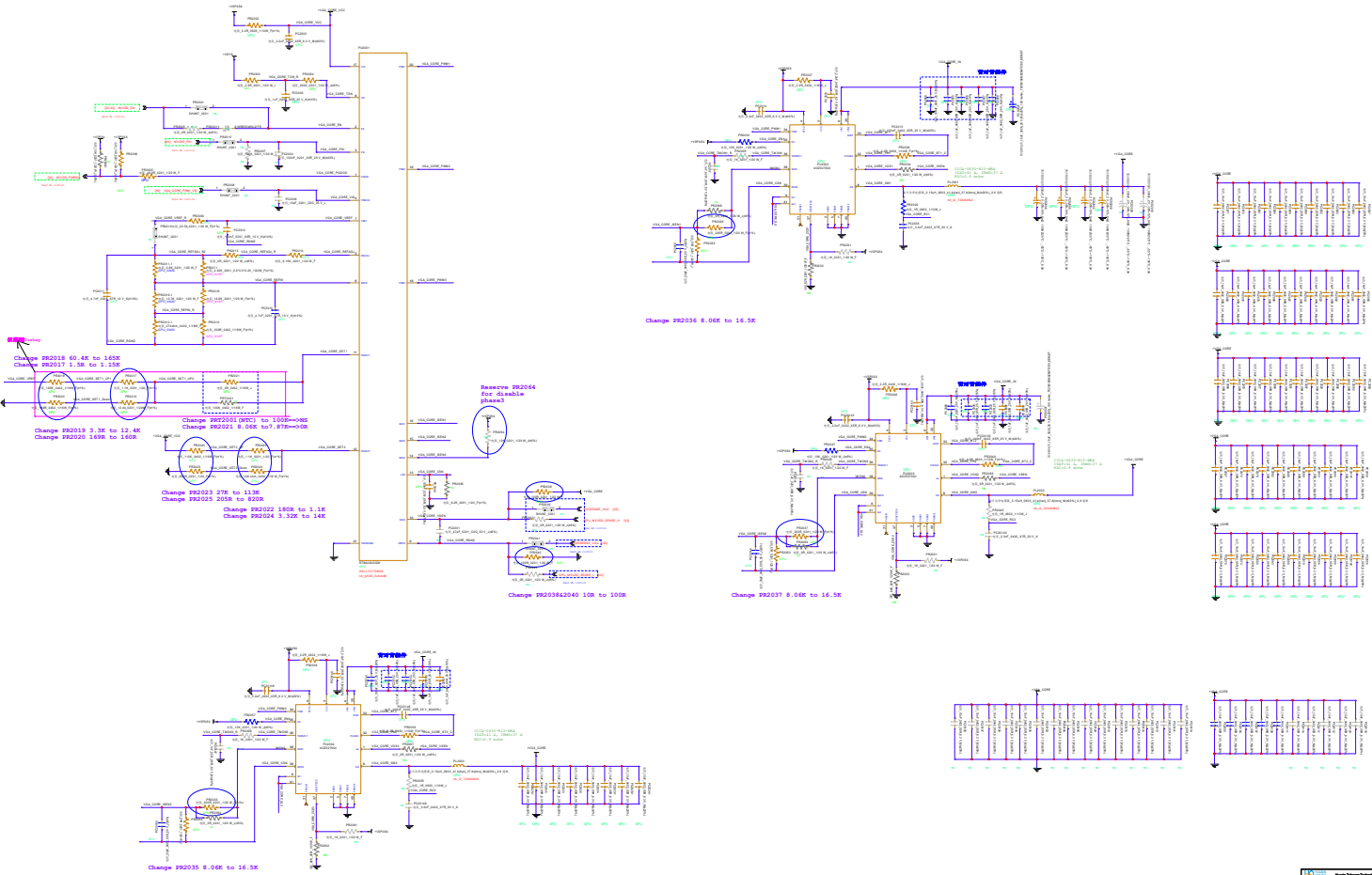
+V1P8SX: Del PQ1901 change to UN7301

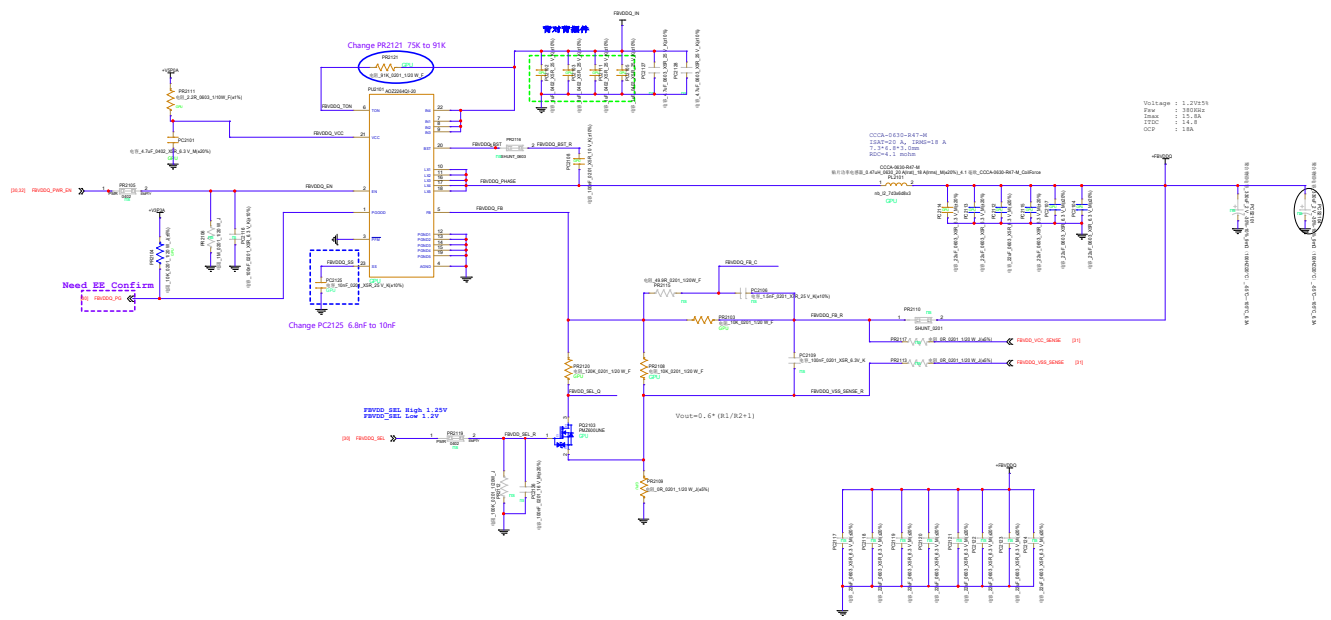
Product	TOP	B2B-Peak	B2B-Continuous
PR20-P1 Max-Q	35W	125.4 A	37.8 A
PR20-P5 Max-Q	35W	125.5 A	37.9 A

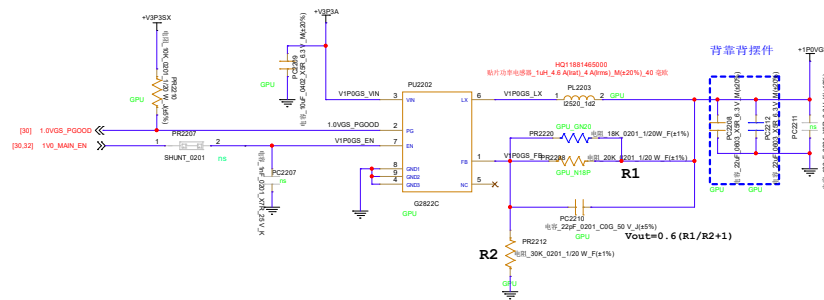
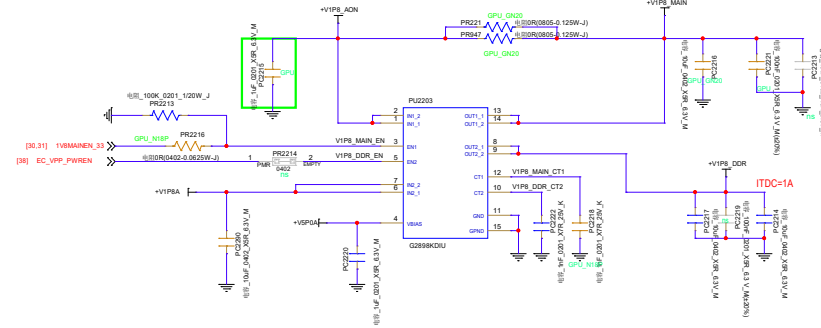
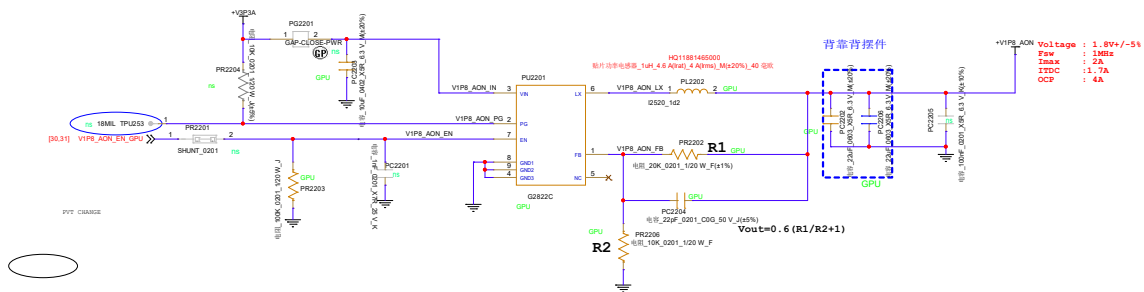
Part	P10002776001				P10002776002				P10002776003			
	PR2036	PR2034	PR2032	PR2037	PR2066	PR2052	PR2035	PR2067	PR2052			
AC/DC/DC	16.5A	0W	NC	16.5A	0W	NC	16.5A	0W	NC			
DC/DC/DC-T1-GE2	16.5A	NC	NC	16.5A	NC	NC	16.5A	NC	NC			
DC/DC/DC/DC	NC	16.5A	16.5A	16.5A	16.5A	16.5A	16.5A	16.5A	16.5A			

Option Table

Part	PR2052 (Phase1)				PR2053 (Phase2)				PR2054 (Phase3)			
	PR2051	PR2058	PR2034	PR2032	PR2037	PR2059	PR2056	PR2052	PR2055	PR2055	PR2057	PR2043
Isley	112932ABD-01	16.5W	NC	NC	16.5W	NC	NC	16.5W	NC	NC	NC	NC





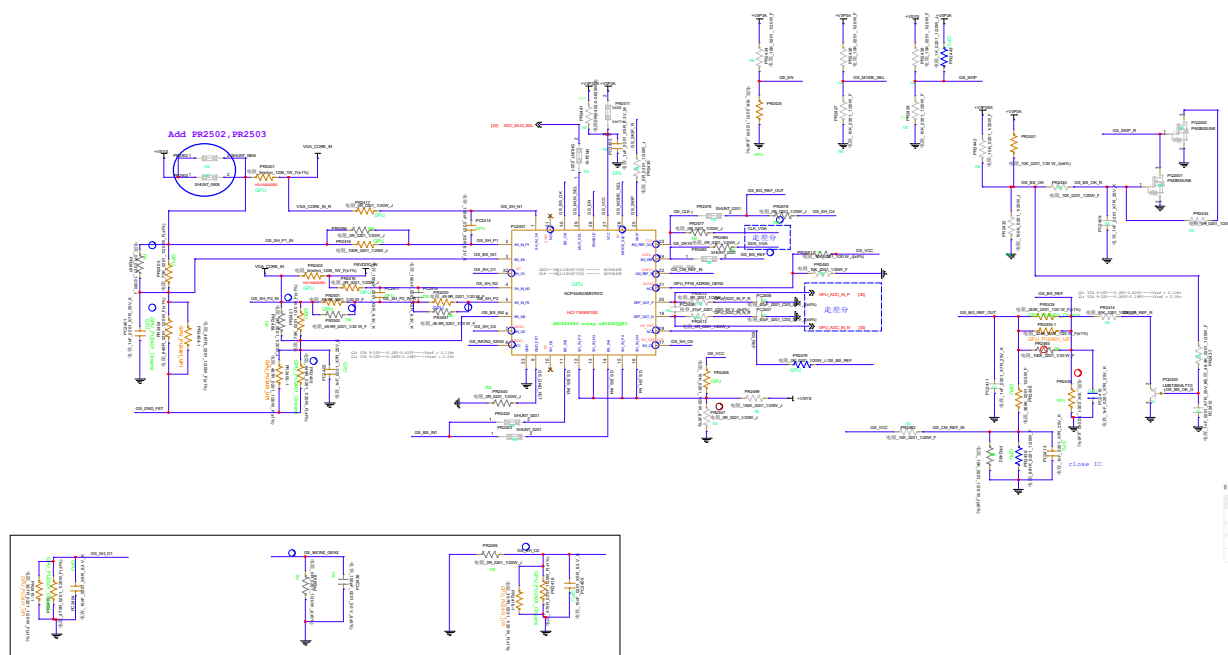


PEX_VDD
N18P_G61 A 1.0V
GN20_P0 0.95V

Voltage : 1.8V+/-5%
Fsw : 1MHz
Imax : 2A
ITDC : 1.6A
OCP : 4A

G1 NCP45492 for N18P
G1 NCP45492 for GN20

[2] SDA_VISA 2
 [2] CL_VISA 1



8 bits I2C address listed below.

ADDR1	ADDR0	Device Address
0	0	0x50
0	1	0x5A
1	0	0x5C
1	1	0x5E

